

FIG 1A

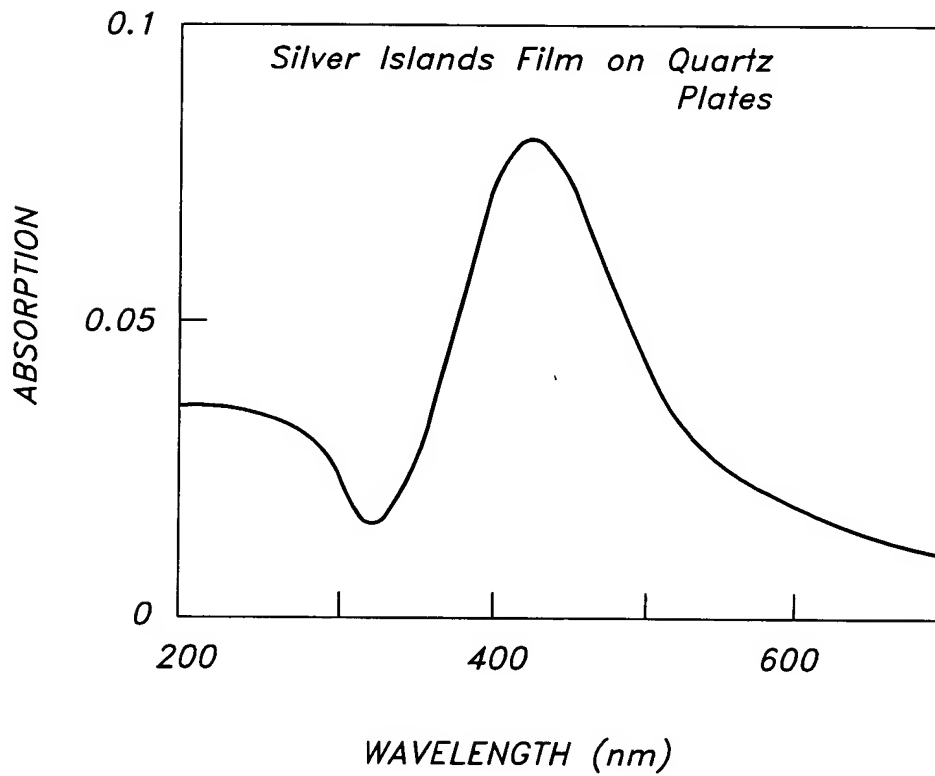
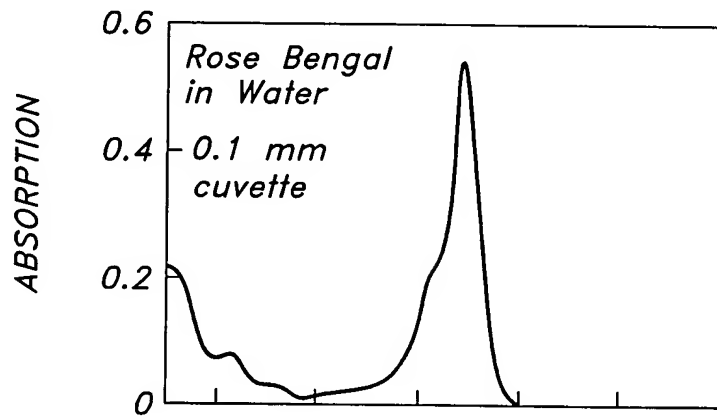
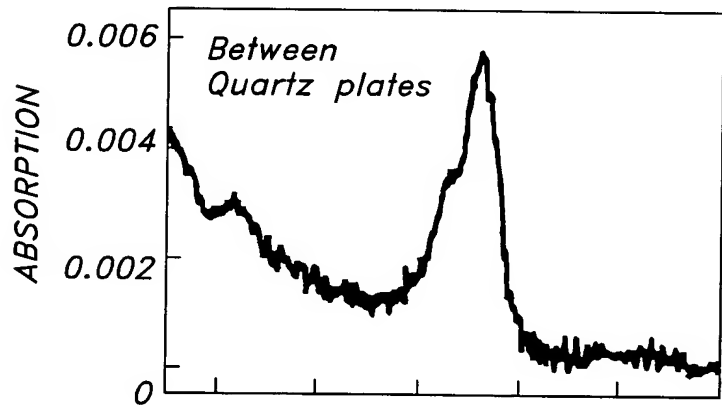
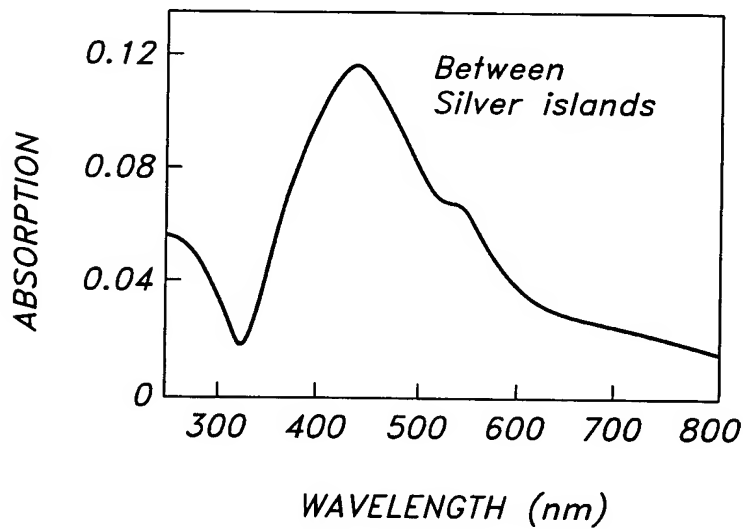
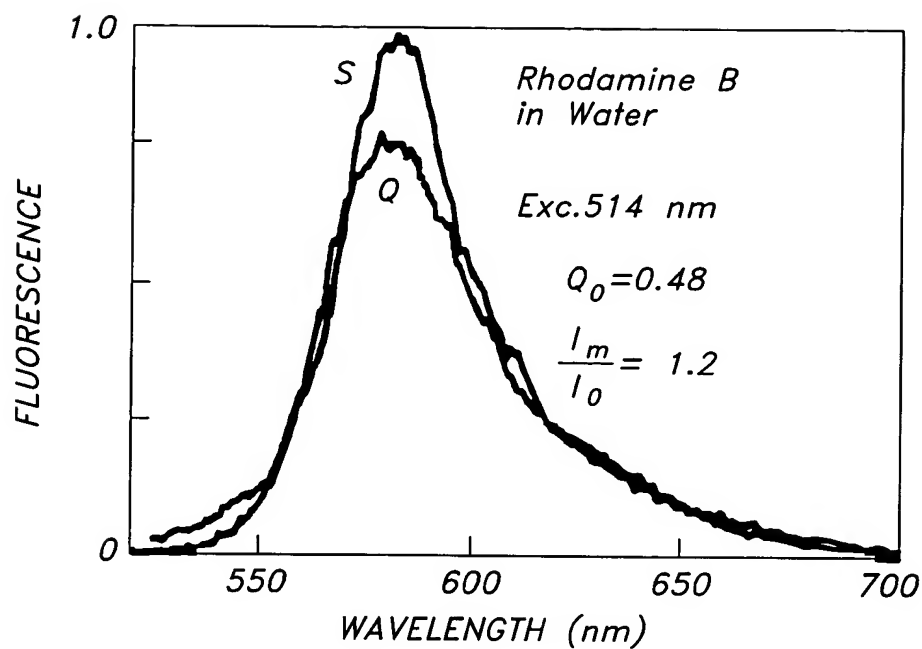
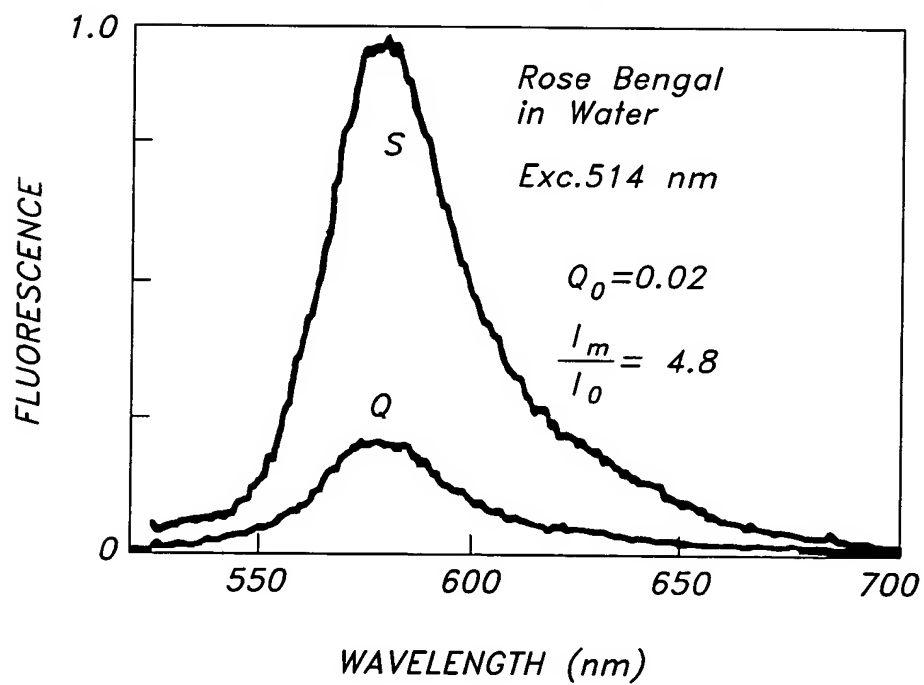
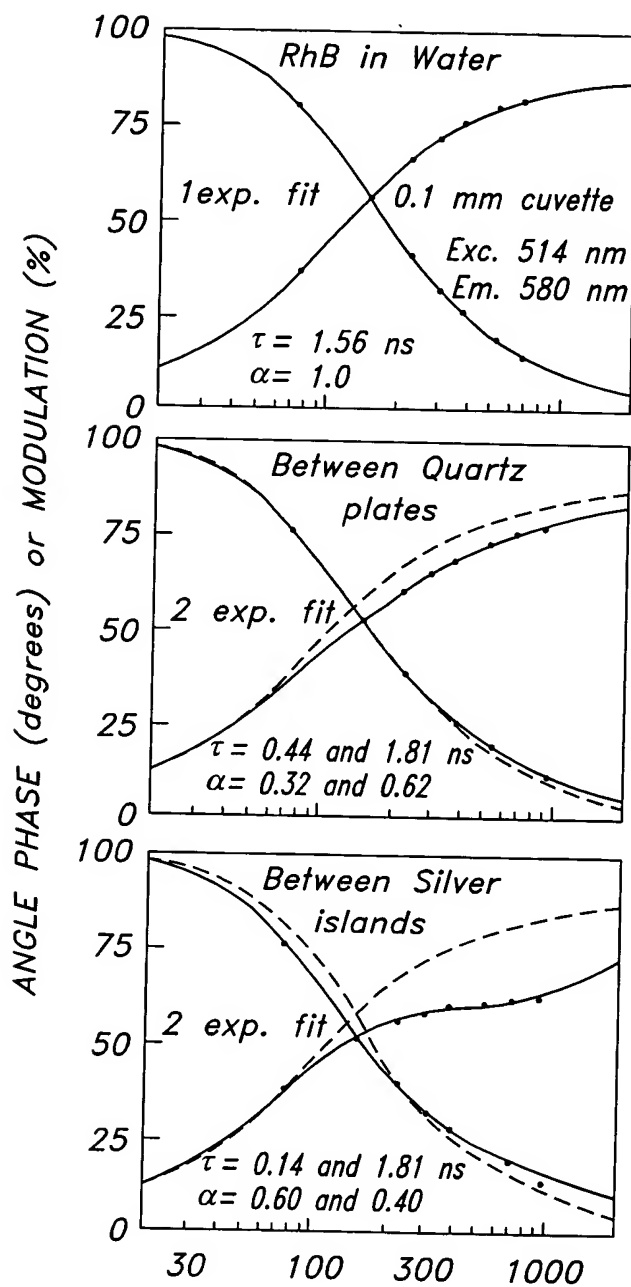
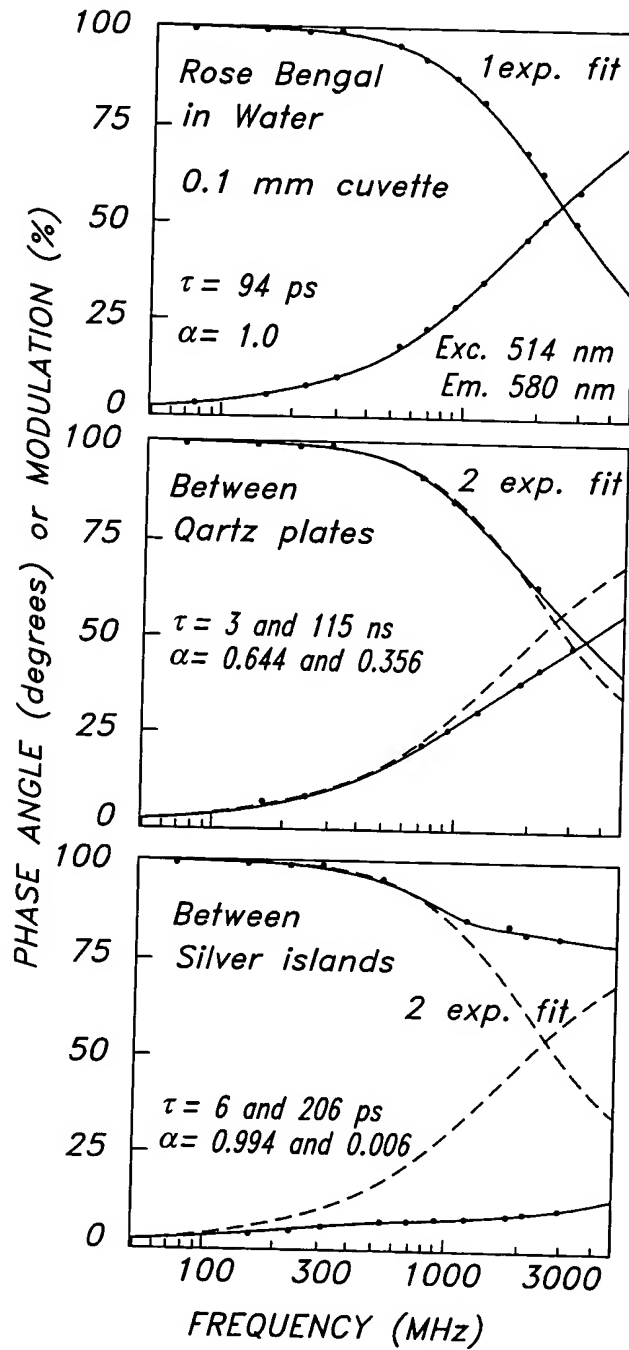


FIG 1B

**FIG 2A****FIG 2B****FIG 2C**

**FIG 3A****FIG 3B**



**FIG 5A****FIG 5B****FIG 5C**

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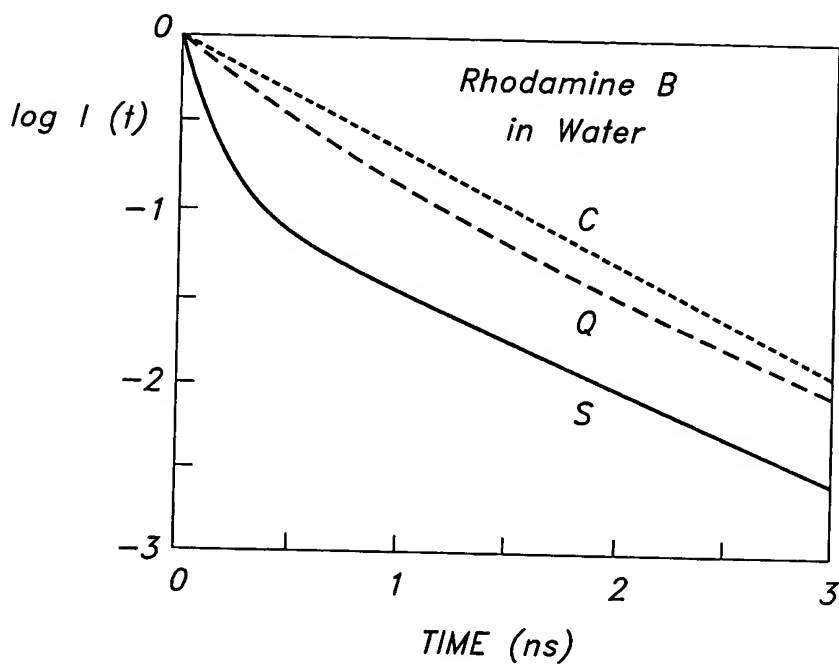


FIG 6A

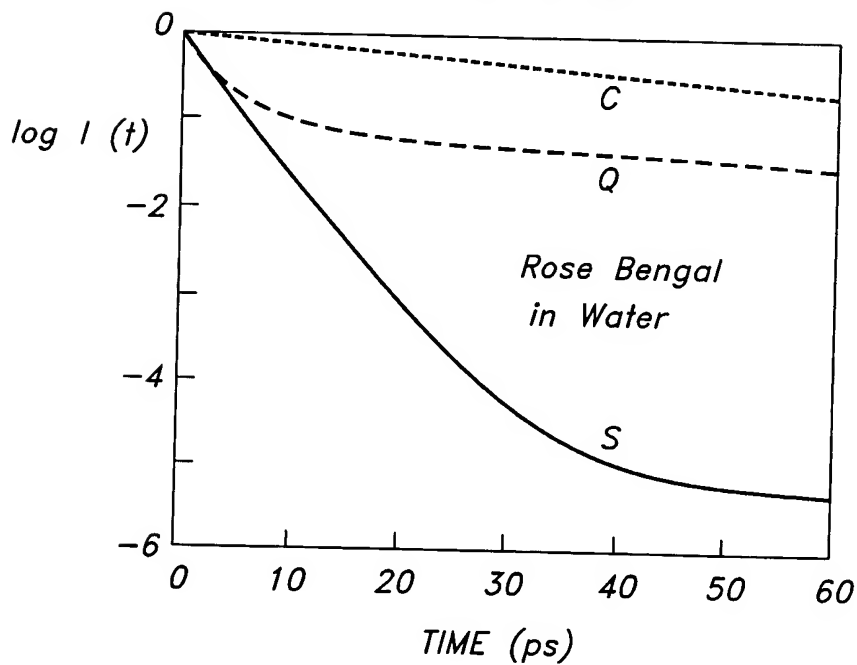
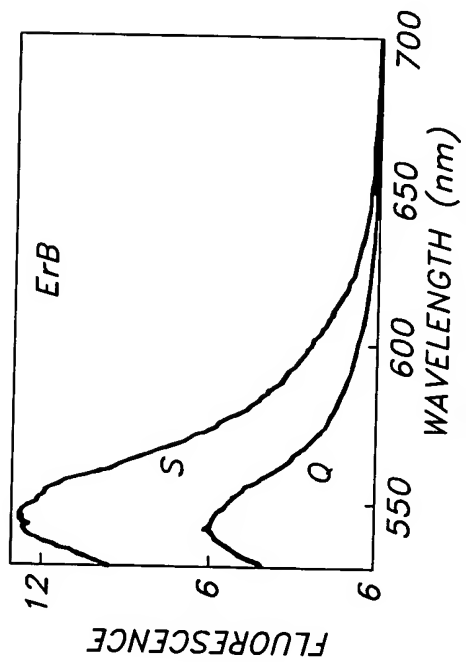
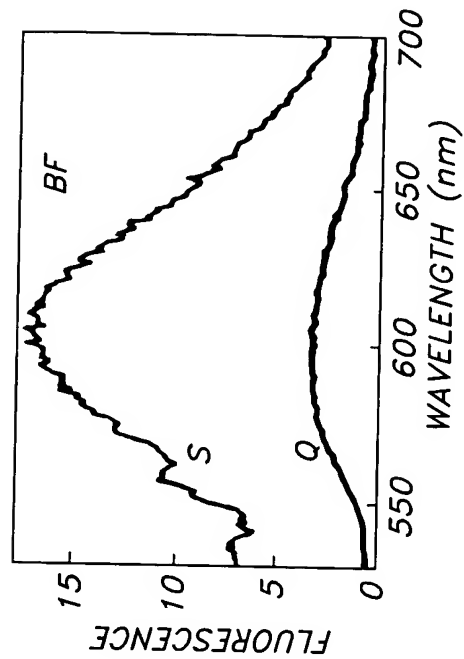
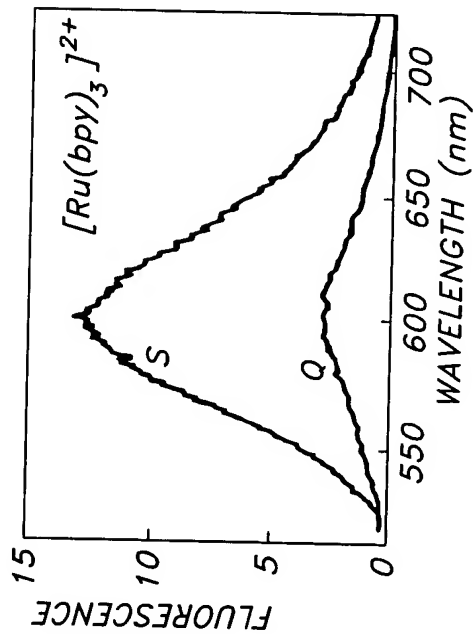
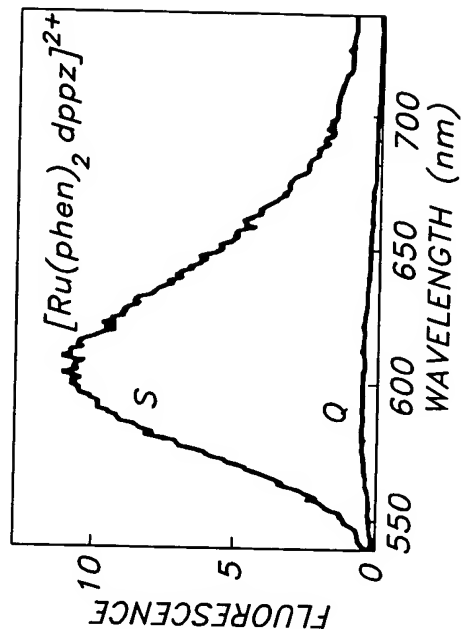


FIG 6B

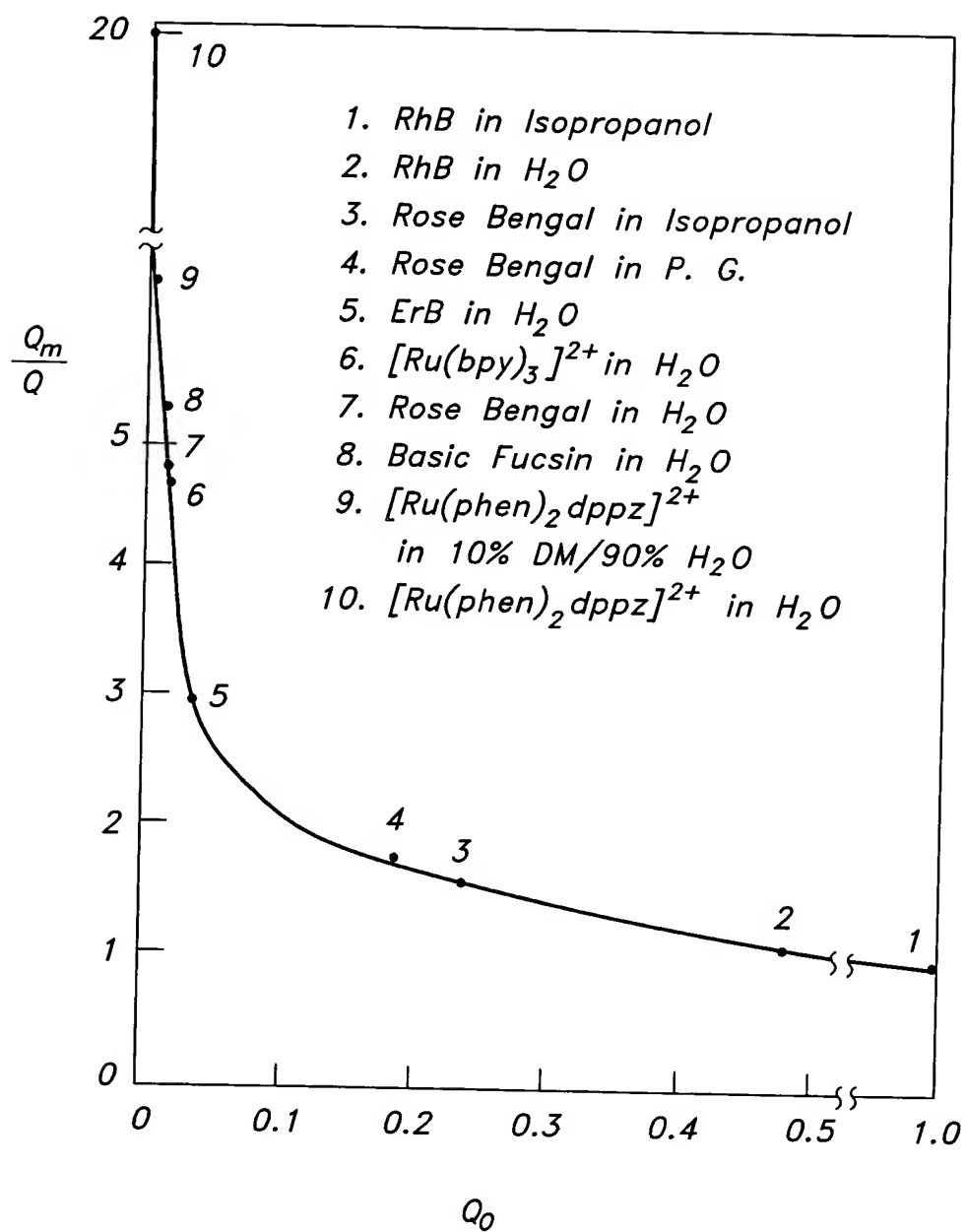
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ACCEPTED MANUSCRIPT

**FIG 7A****FIG 7B****FIG 7C****FIG 7D**

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**FIG 8**

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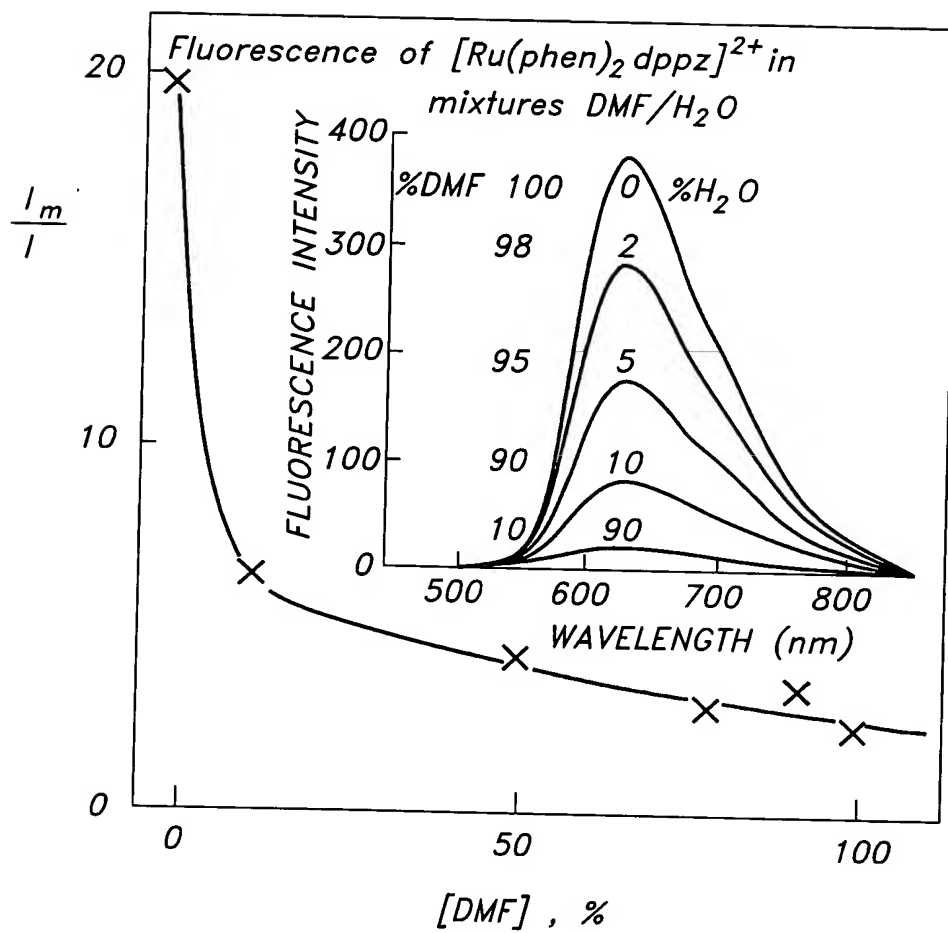


FIG 9

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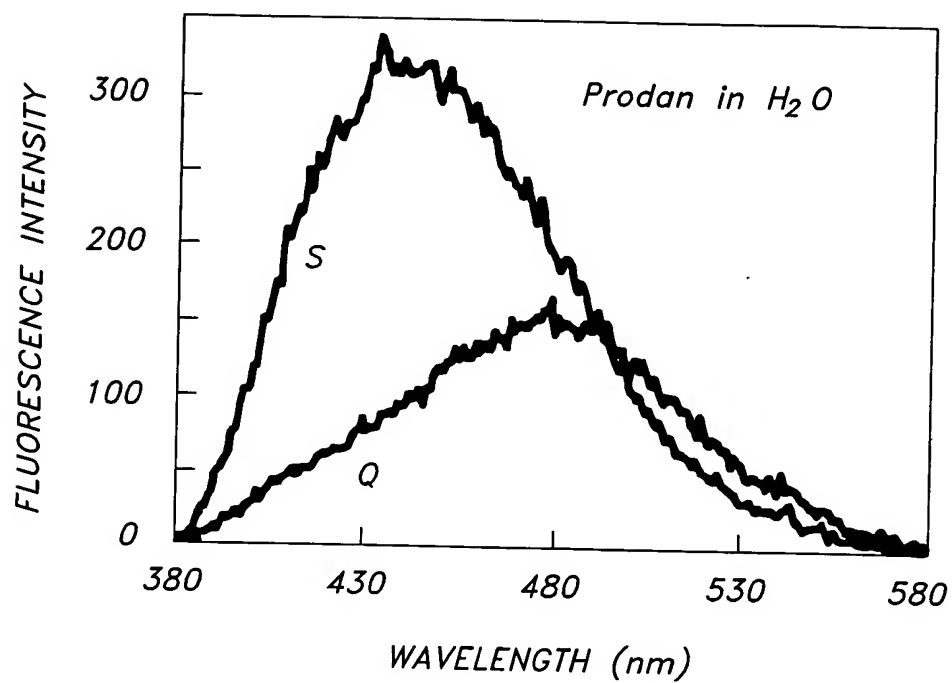


FIG 10A

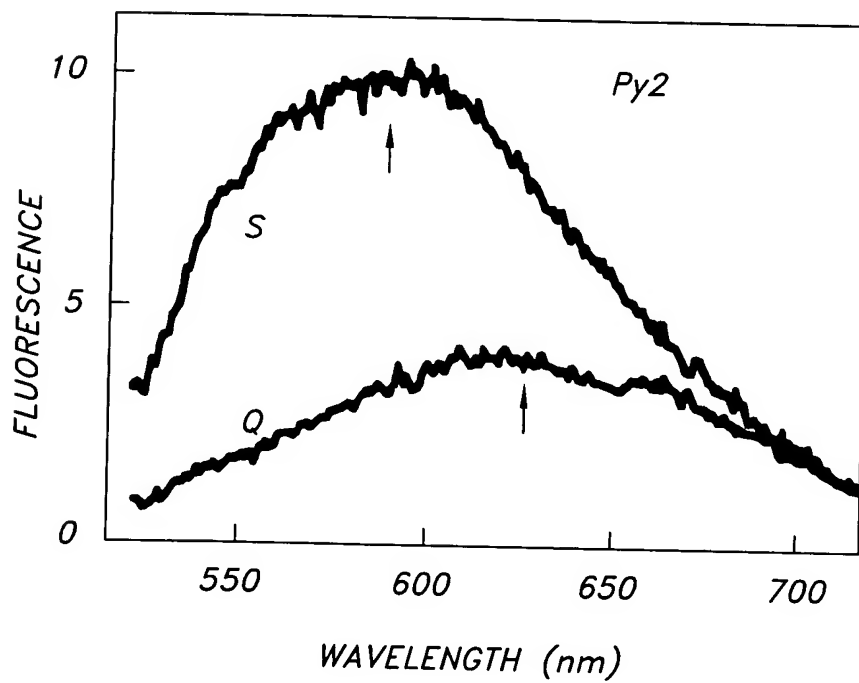


FIG 10B

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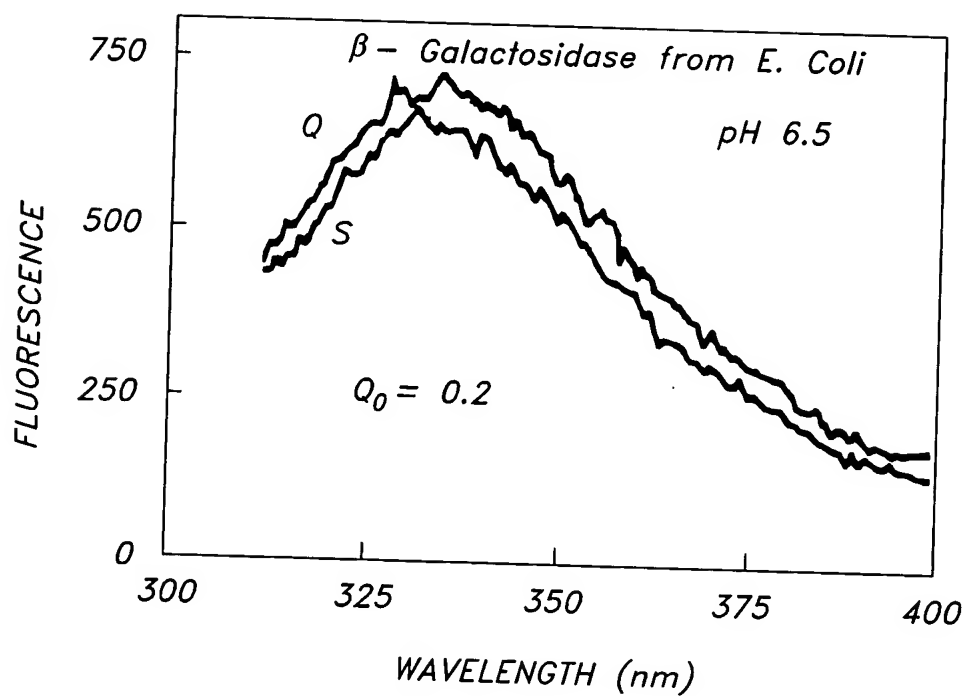


FIG 11A

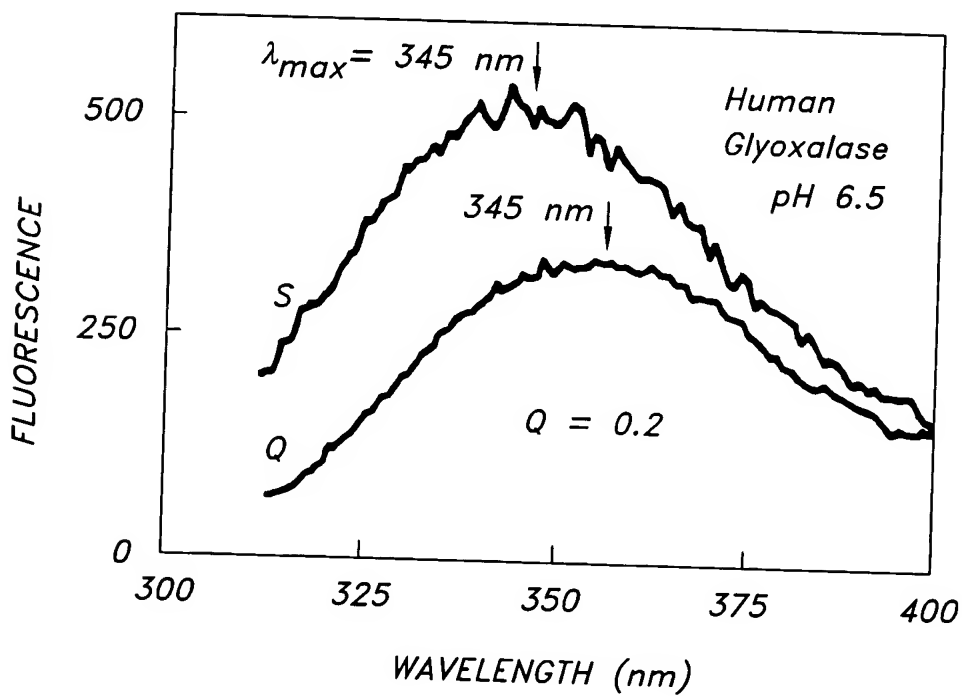


FIG 11A

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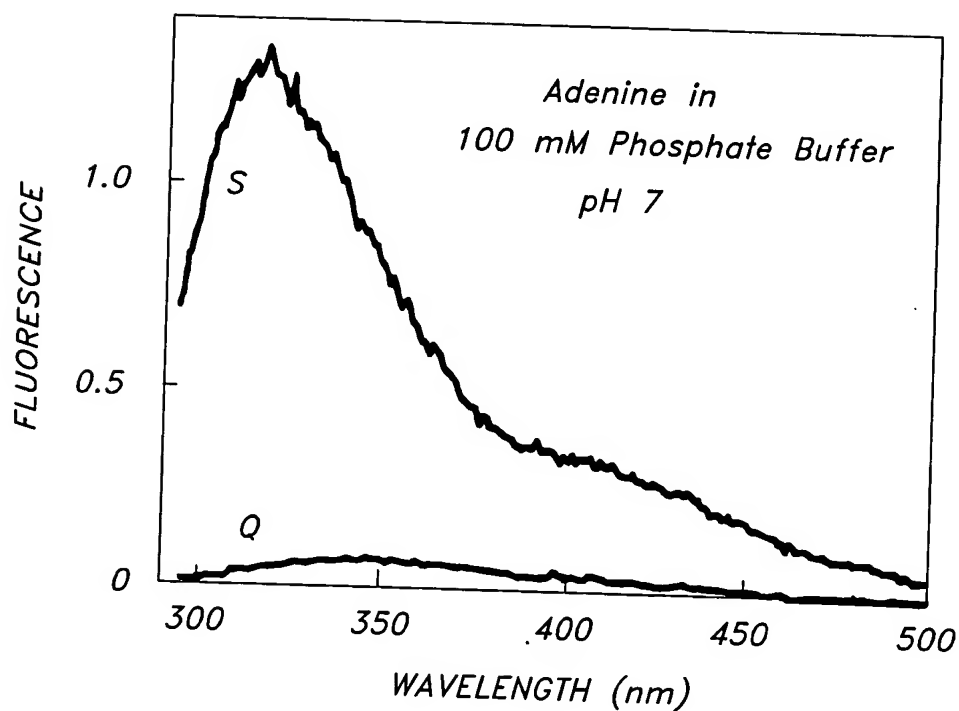


FIG 12A

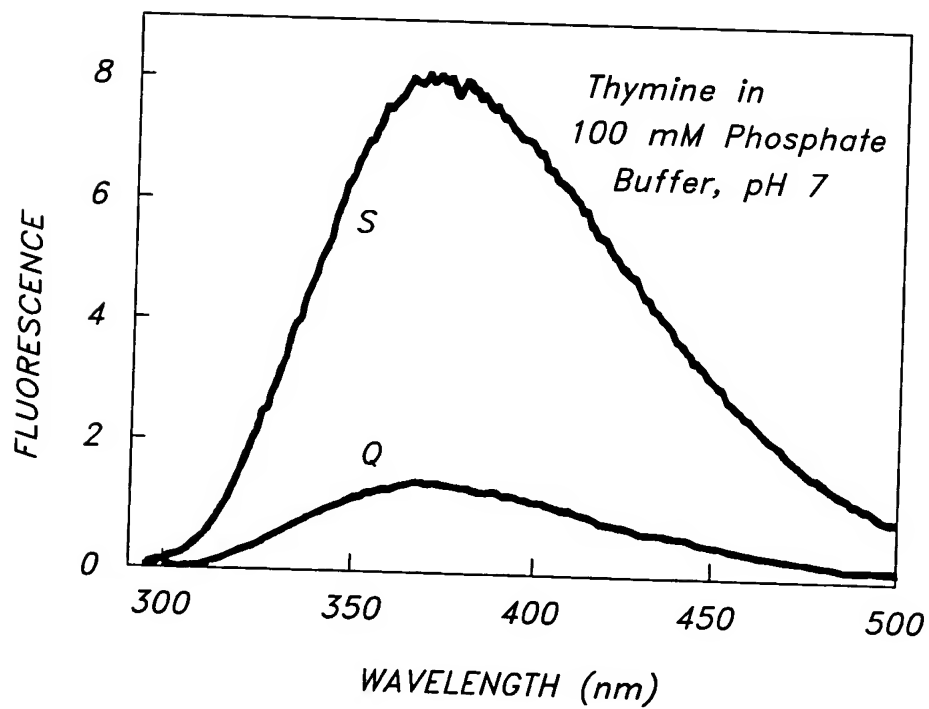
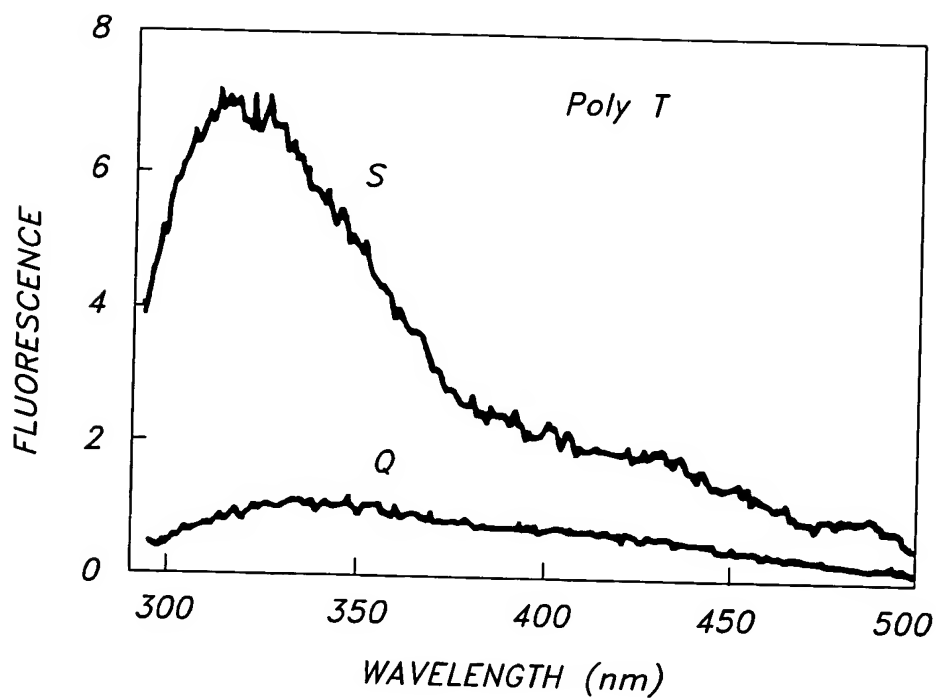
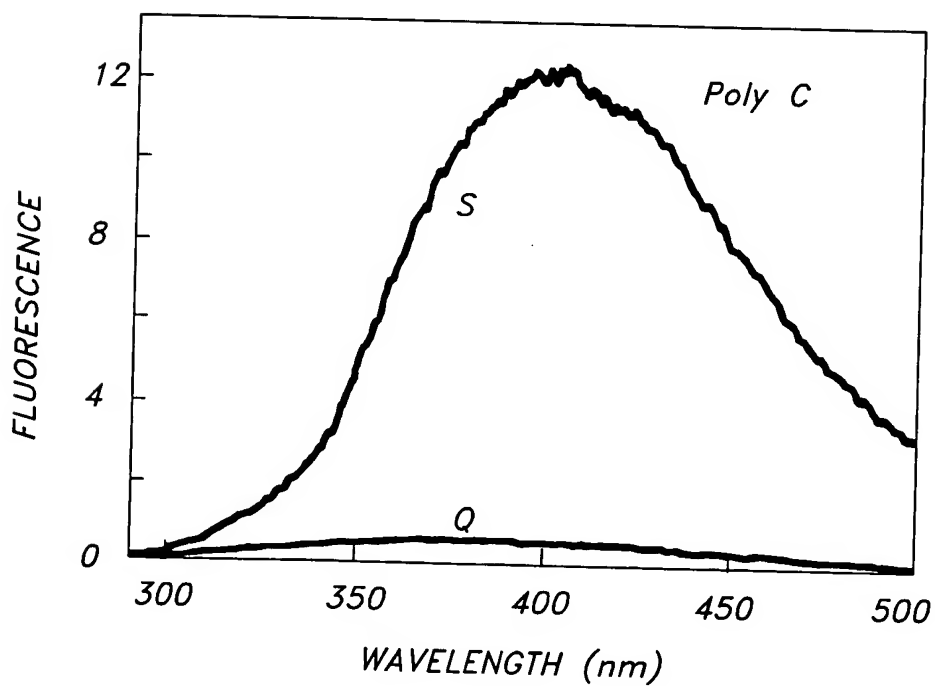


FIG 12B

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**FIG 13A****FIG 13B**

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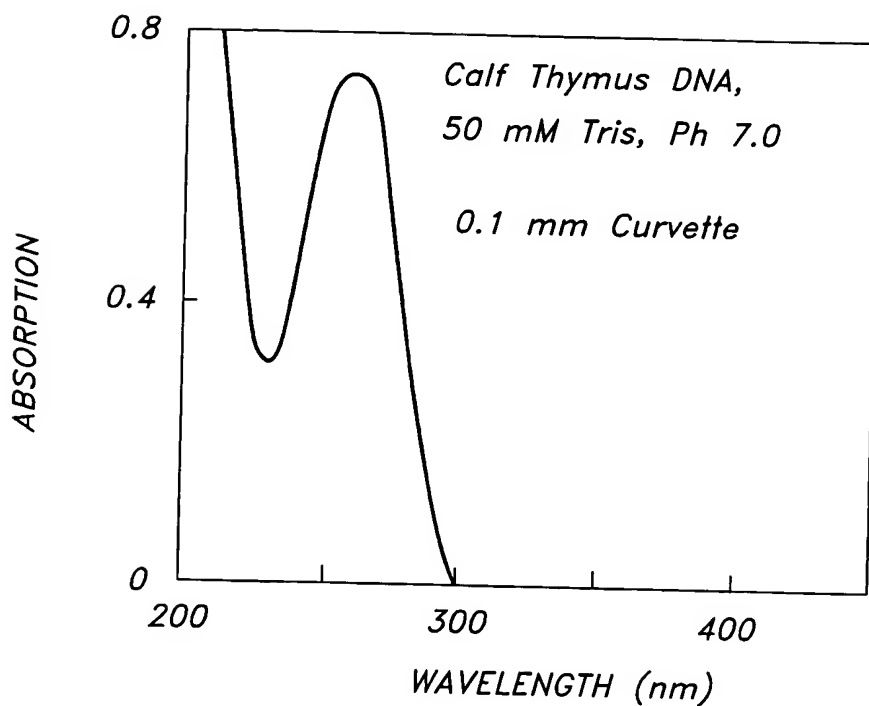


FIG 14A

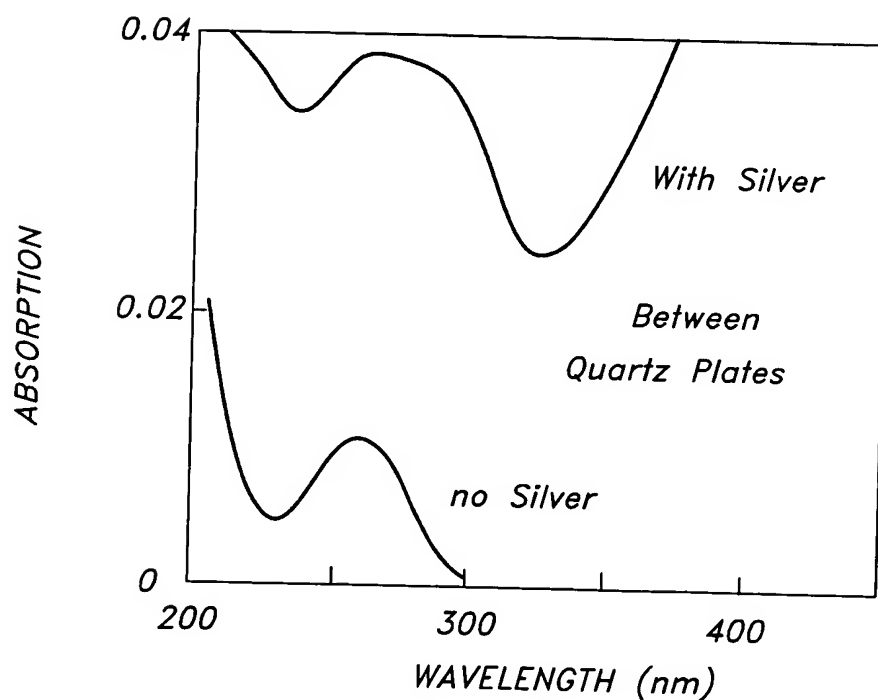


FIG 14B

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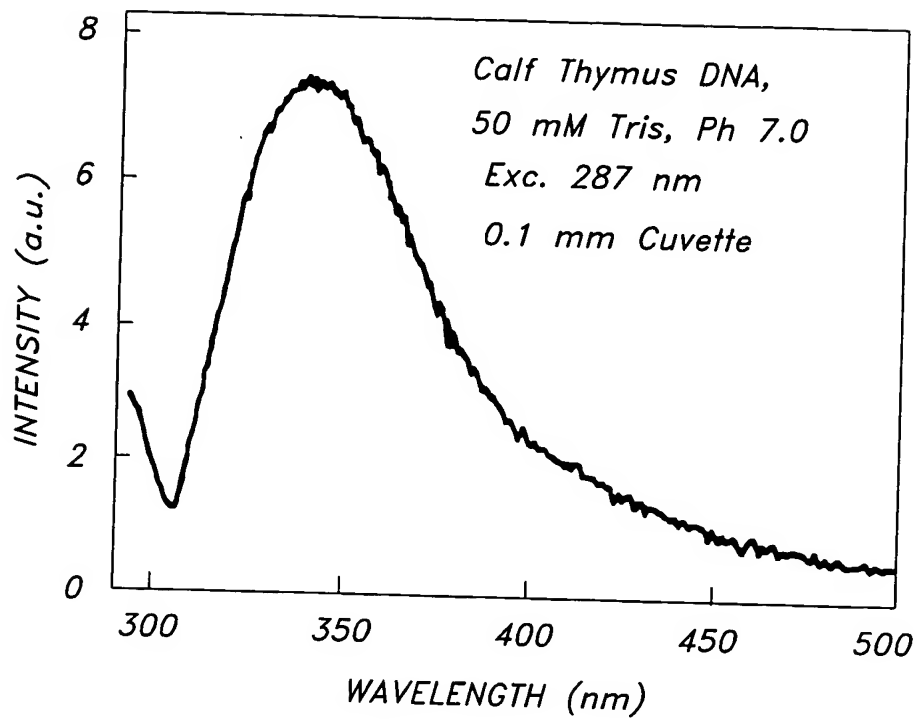


FIG 15A

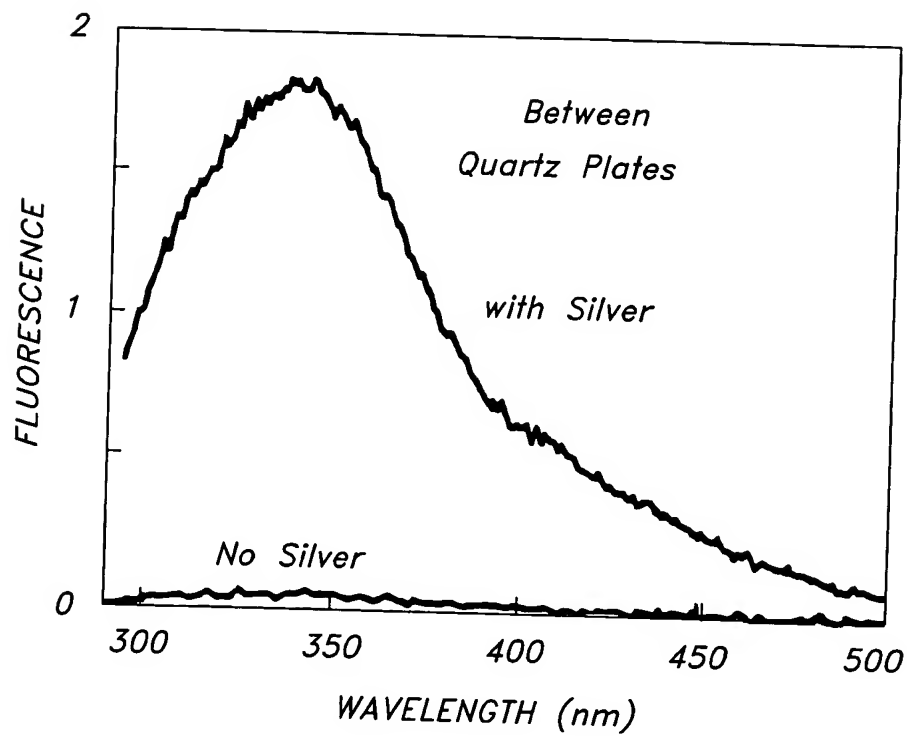


FIG 15B

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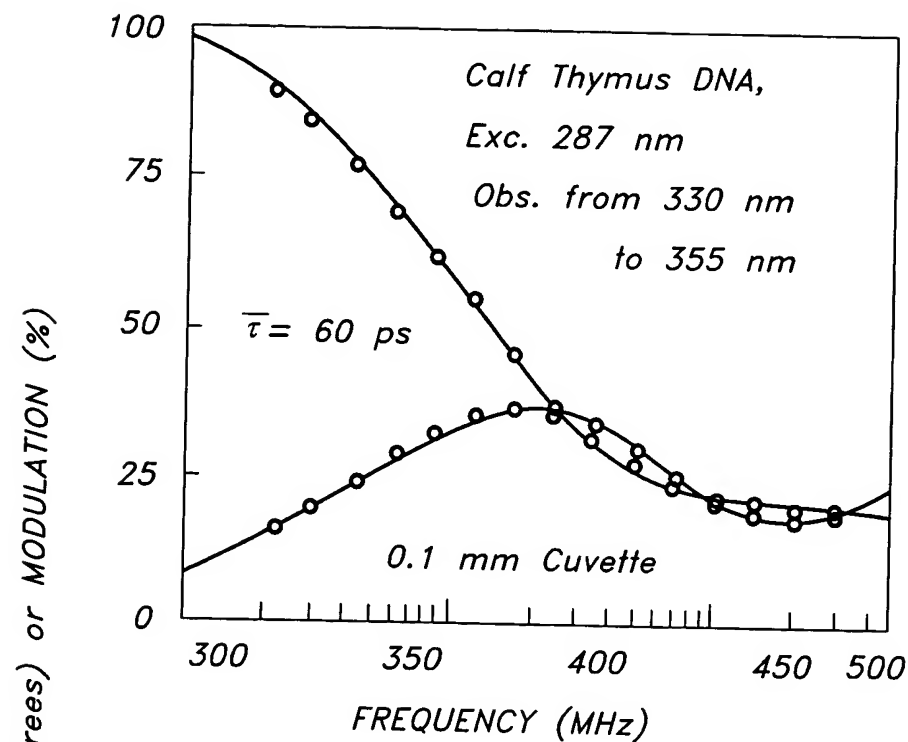


FIG 16A

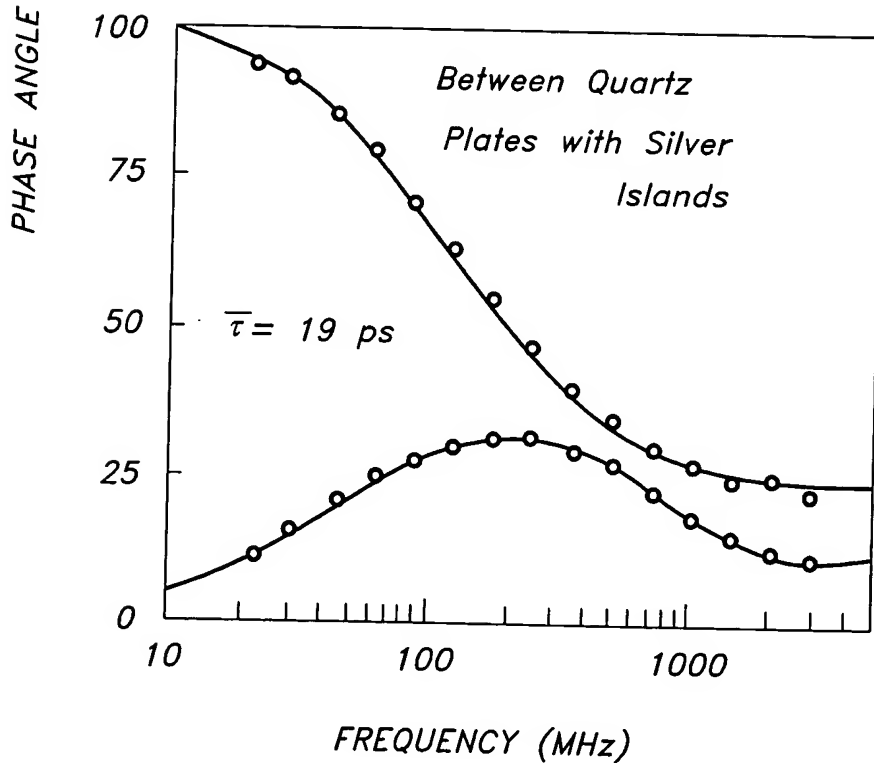
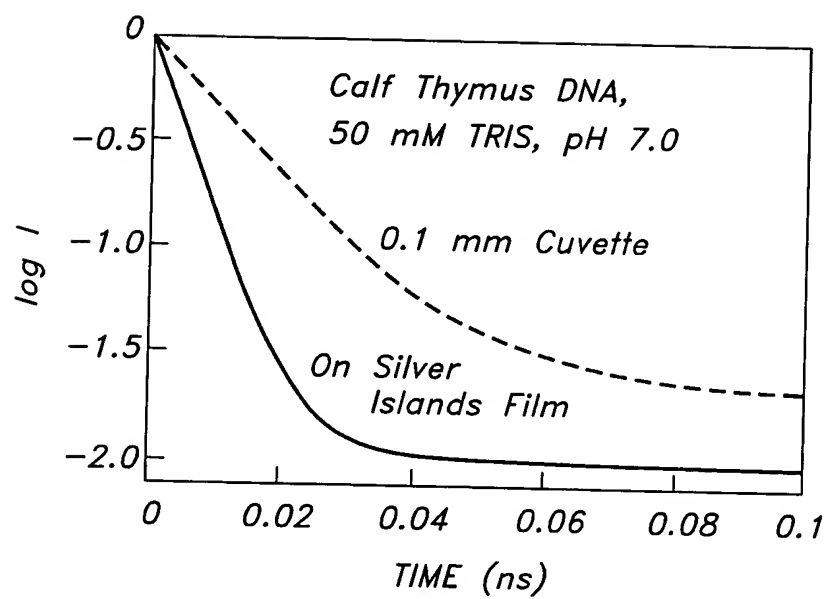
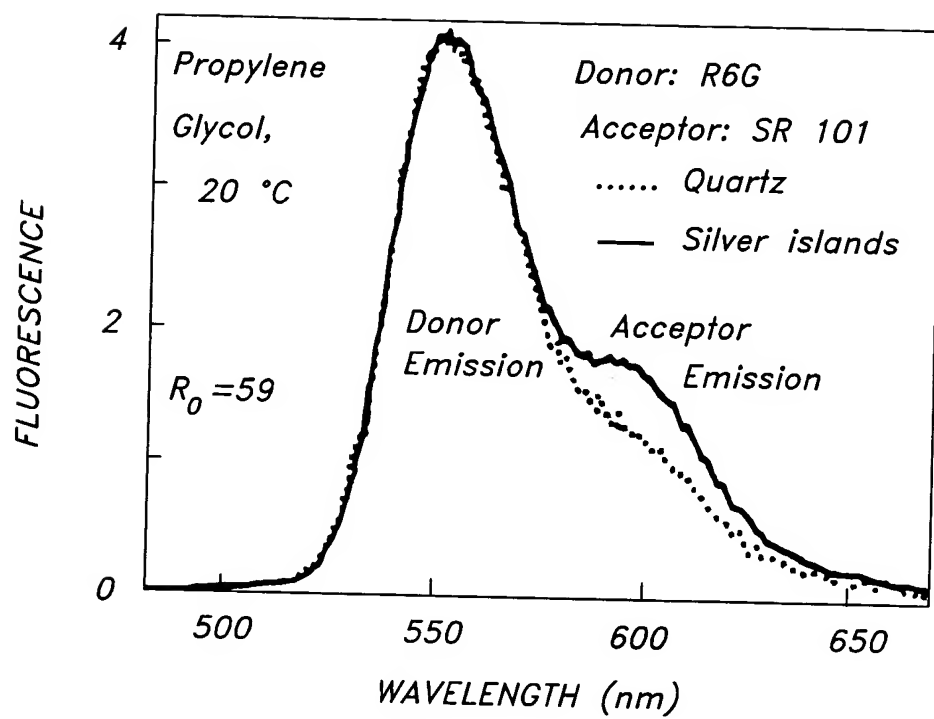
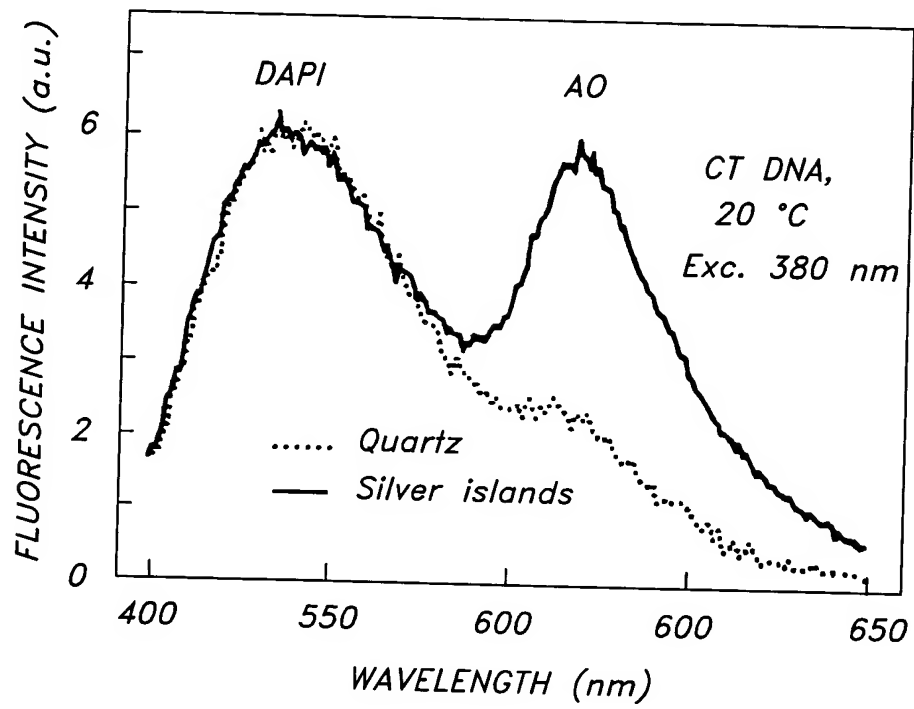
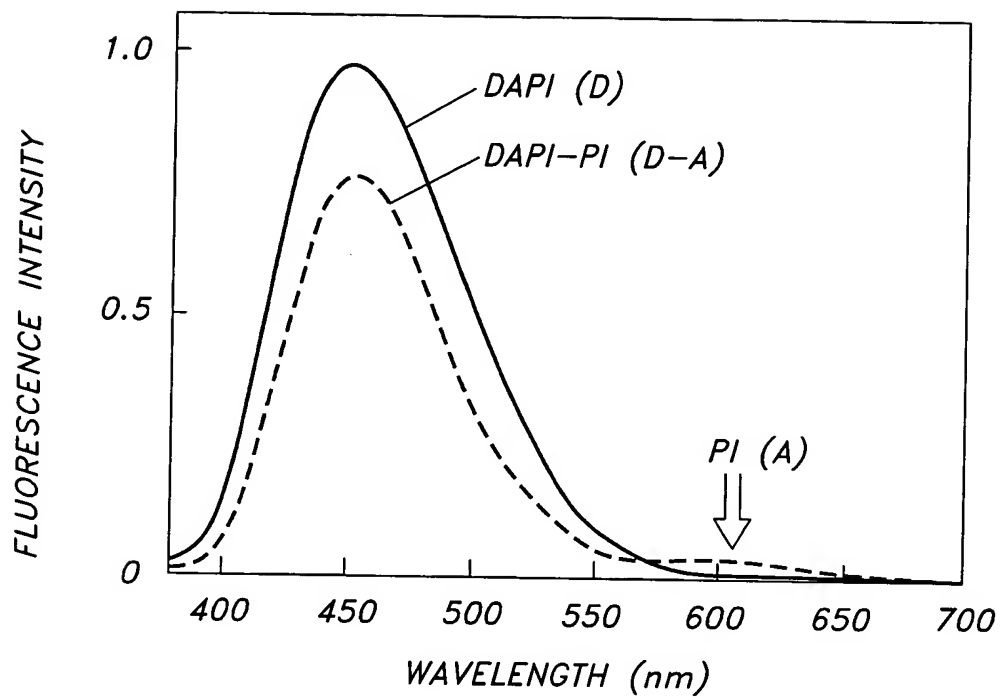


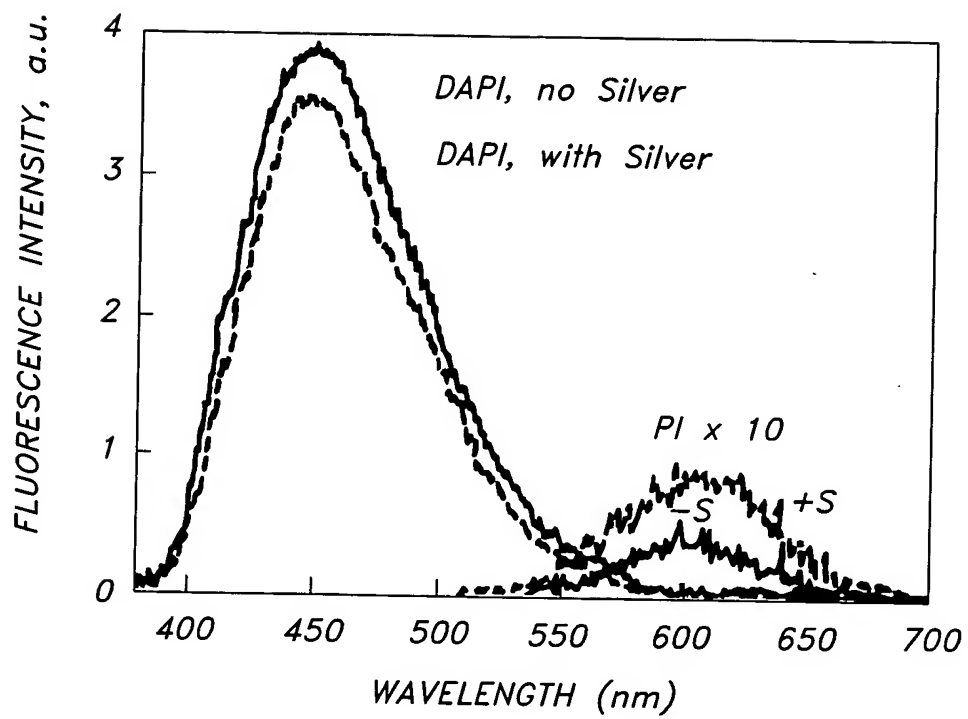
FIG 16B

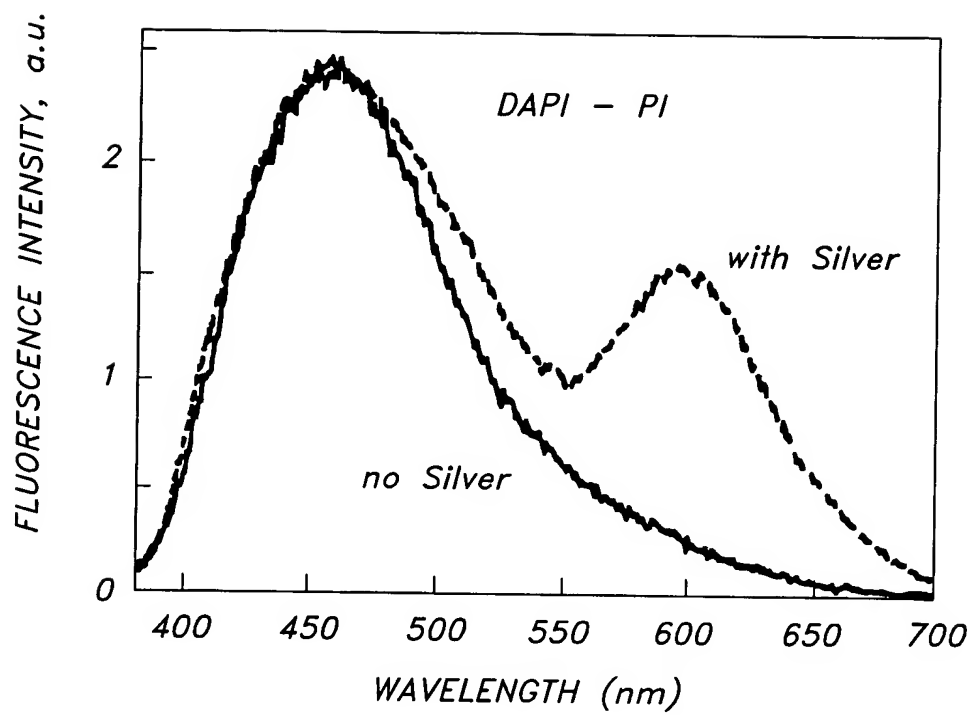
**FIG 17**

**FIG 18**

**FIG 19**

**FIG 20**

**FIG 21**

**FIG 22**

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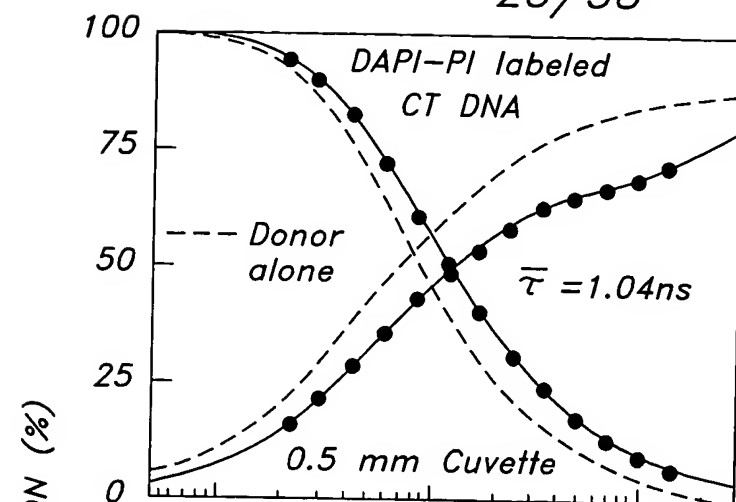


FIG 23A

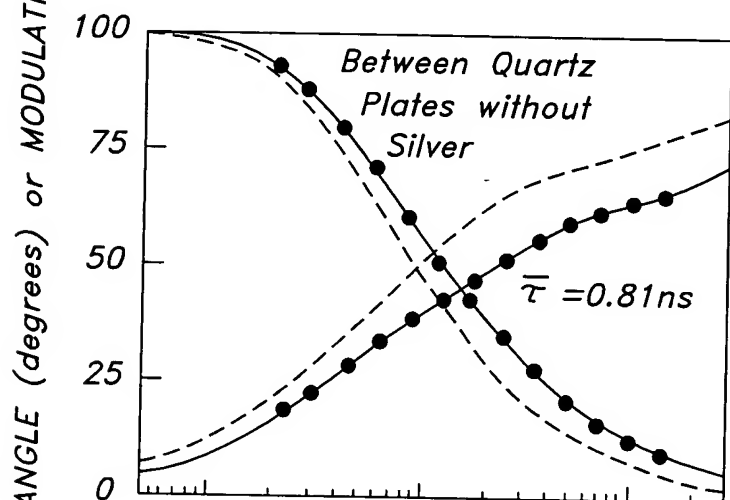


FIG 23B

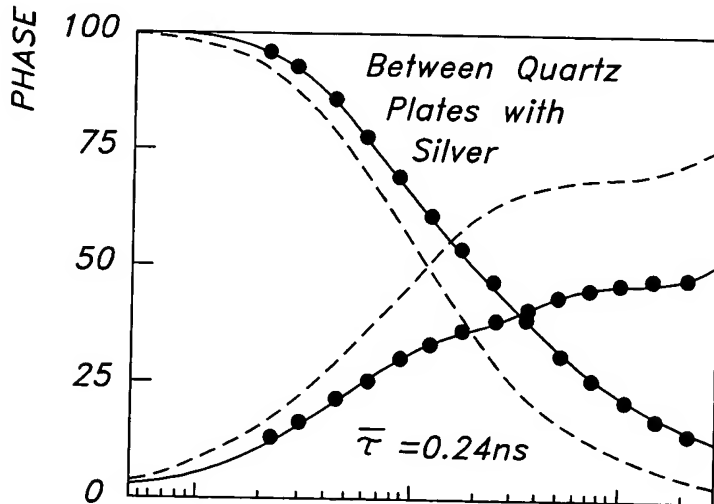


FIG 23C

FREQUENCY (MHz)

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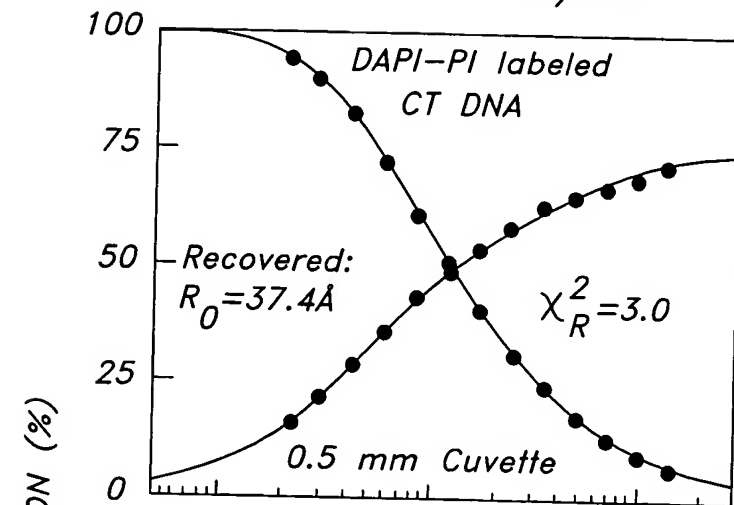


FIG 24A

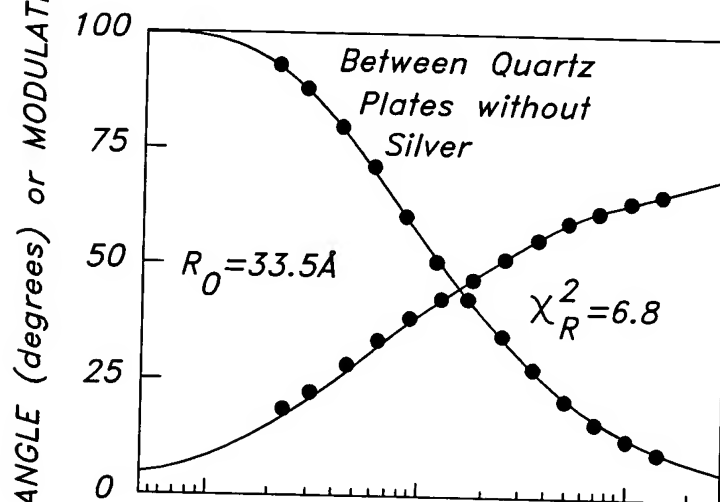


FIG 24B

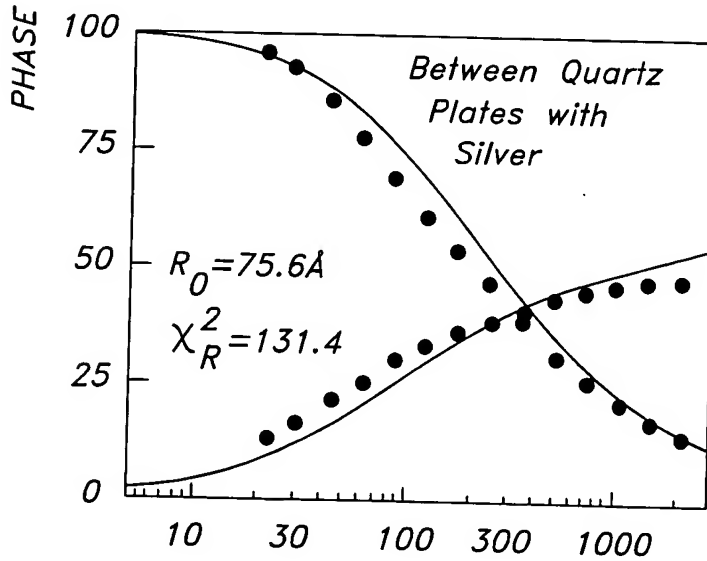
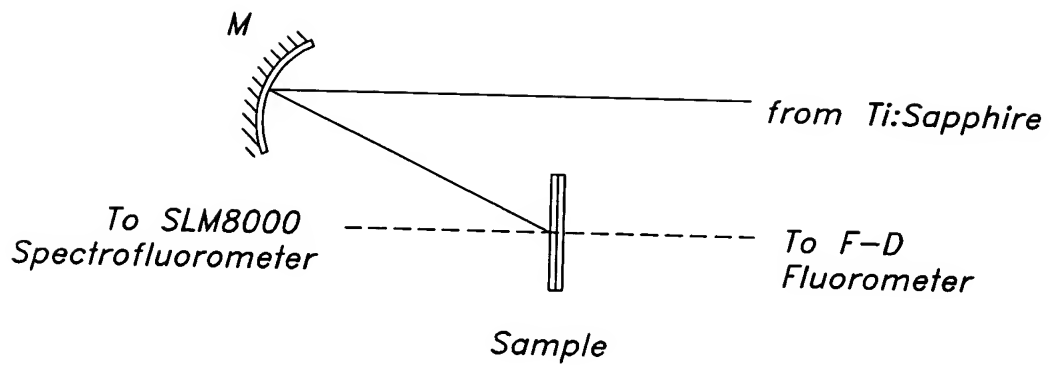
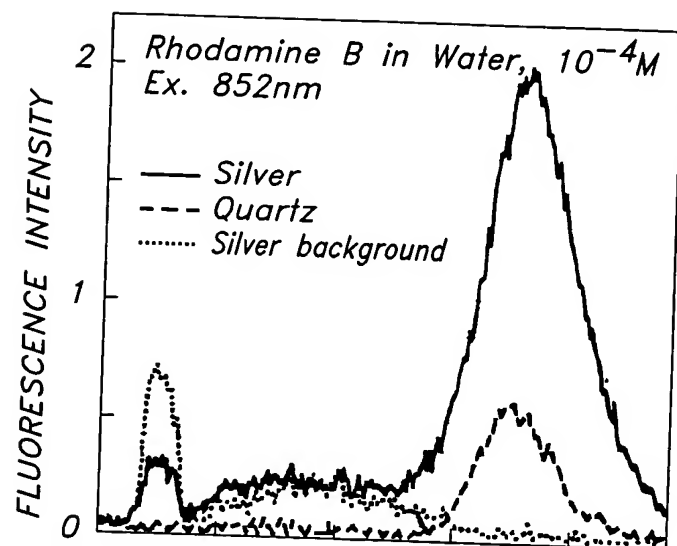
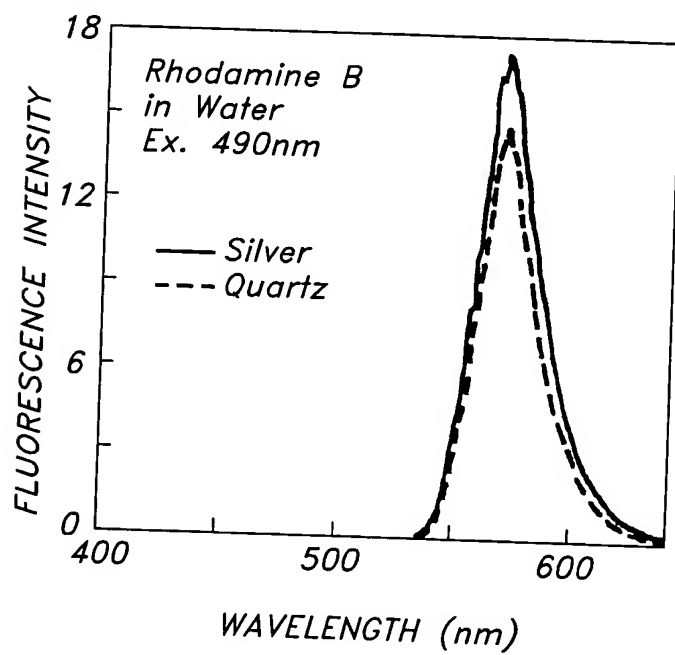
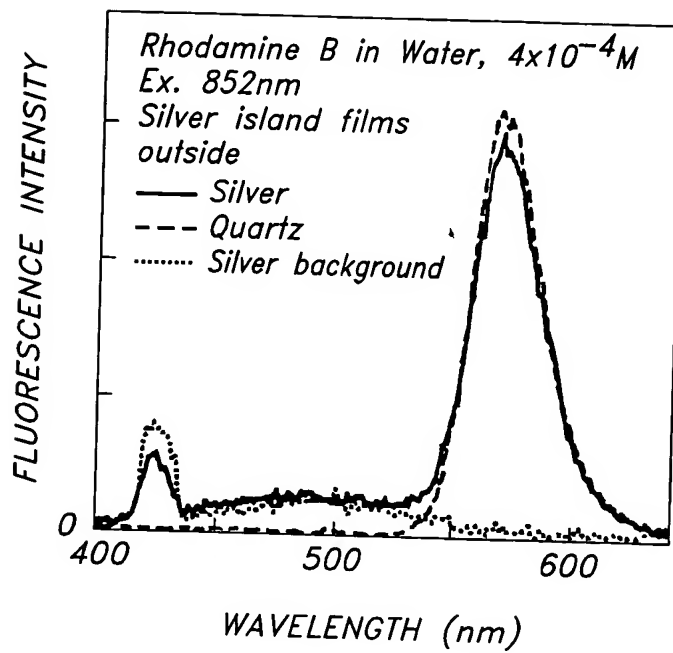
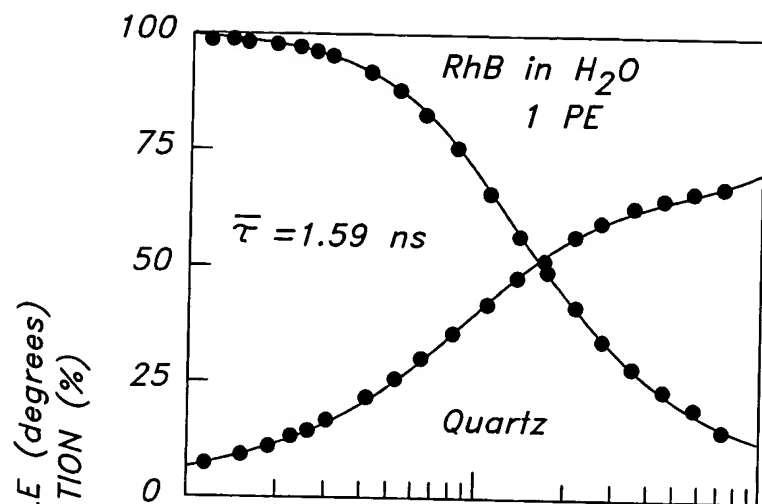
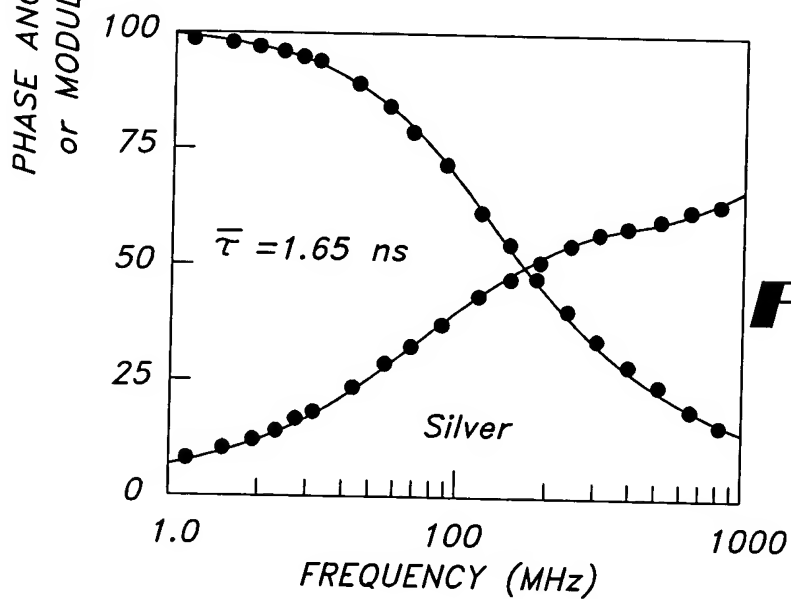


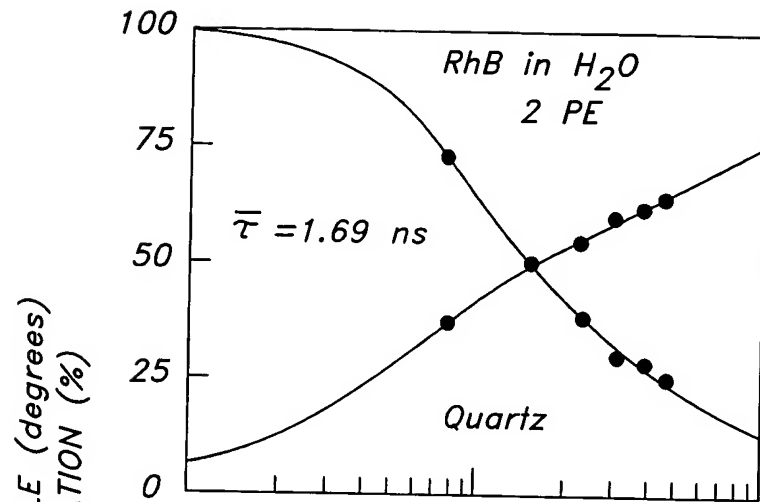
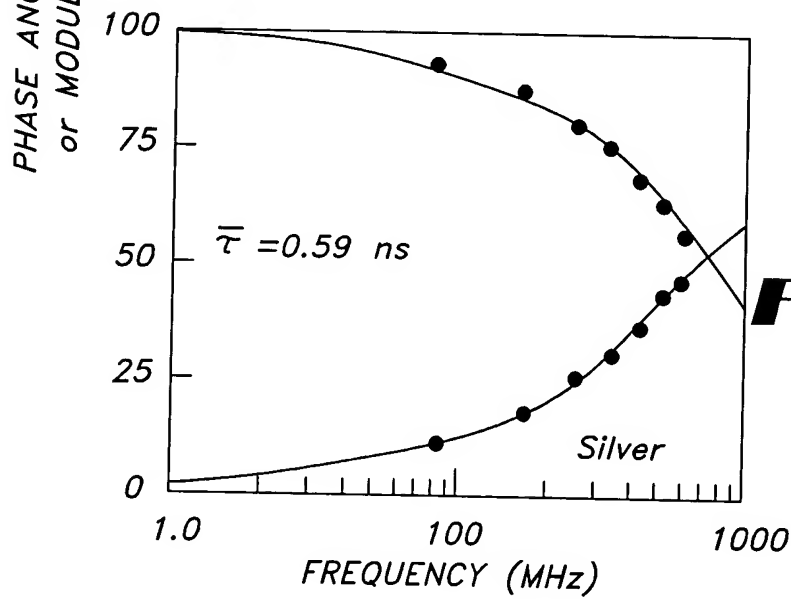
FIG 24C

**FIG 25**

**FIG 26A****FIG 26B**

**FIG 27**

**FIG 28A****FIG 28B**

**FIG 29A****FIG 29B**

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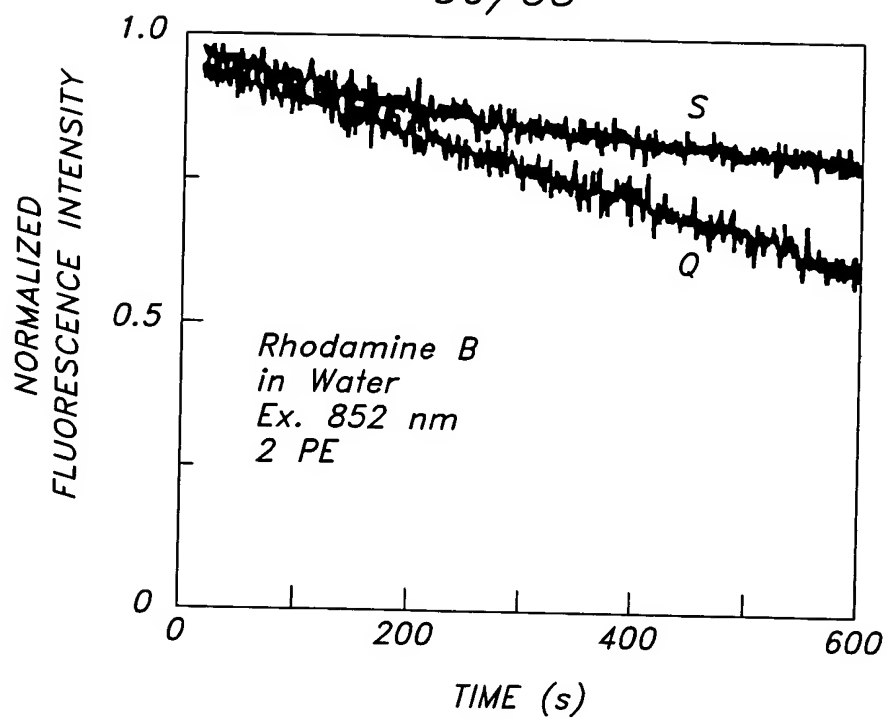


FIG 30A

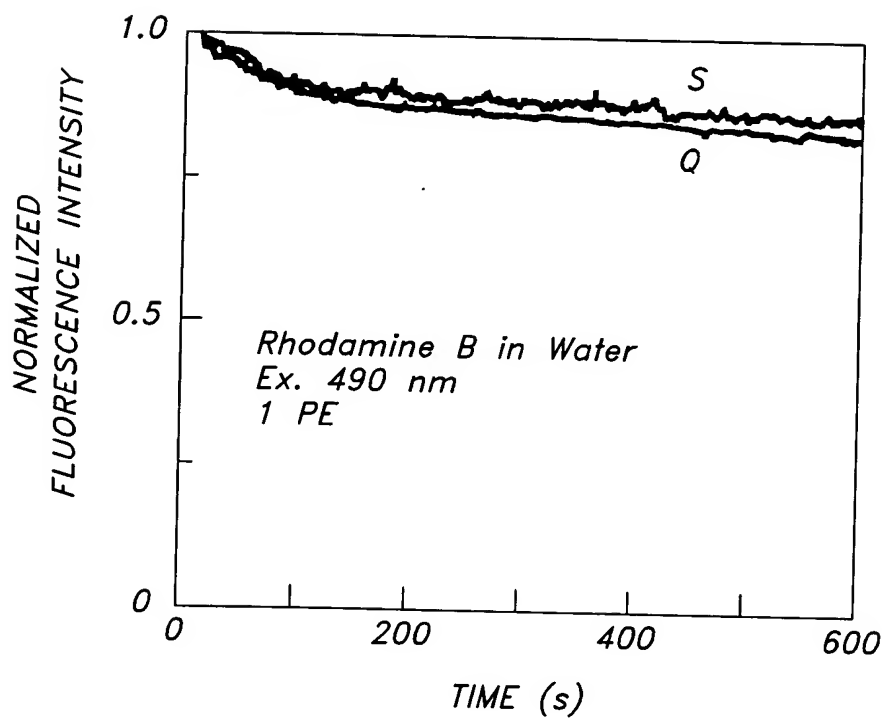


FIG 30B

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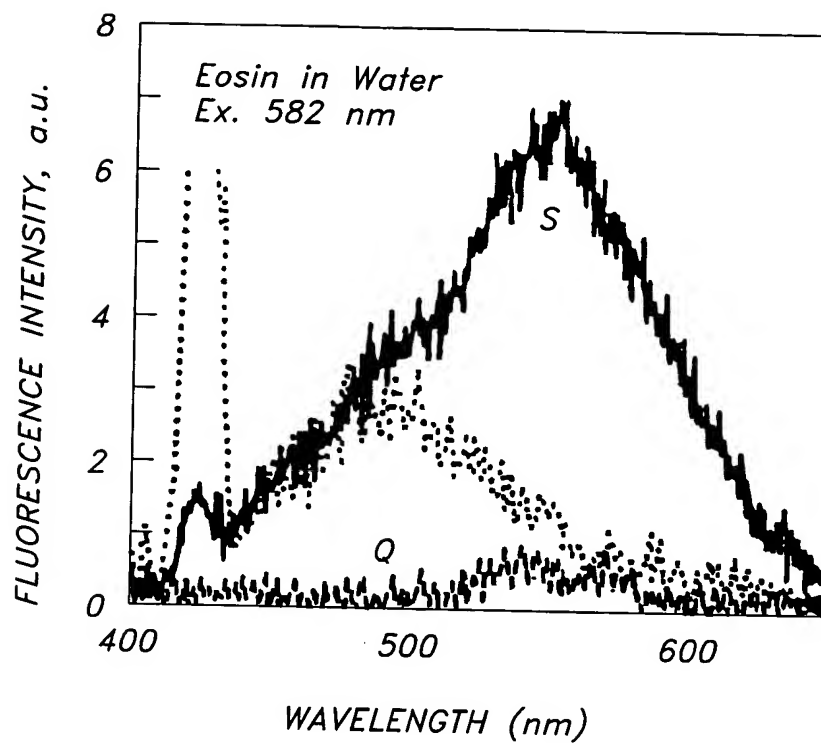


FIG 31A

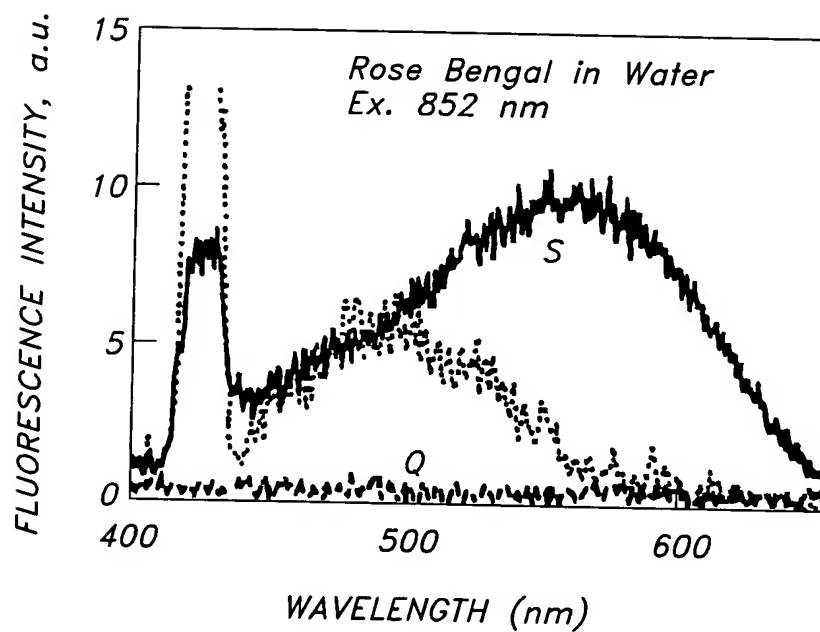


FIG 31B

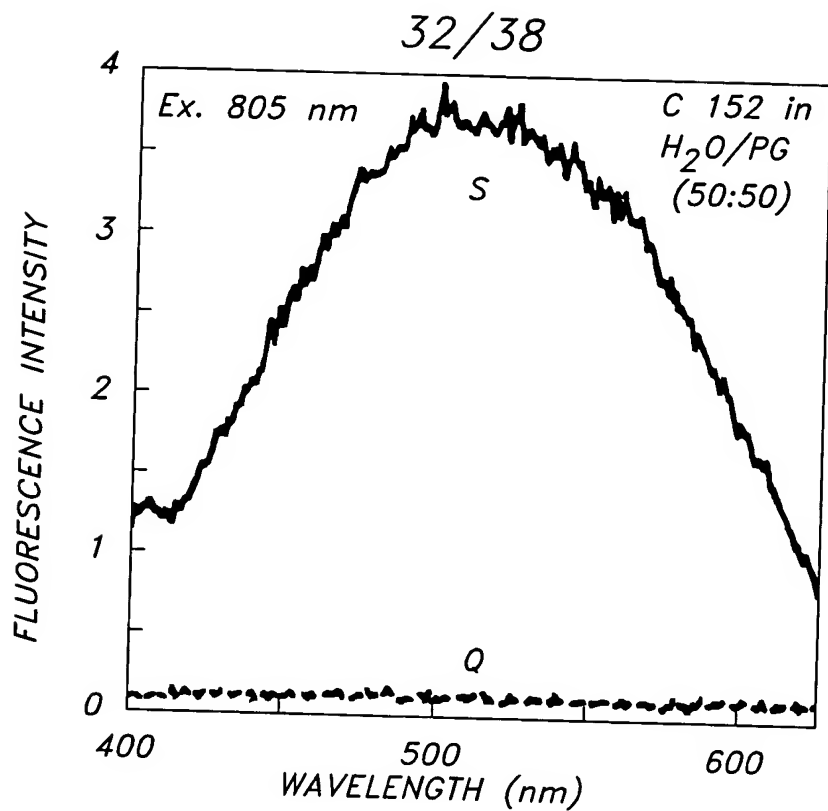


FIG 32A

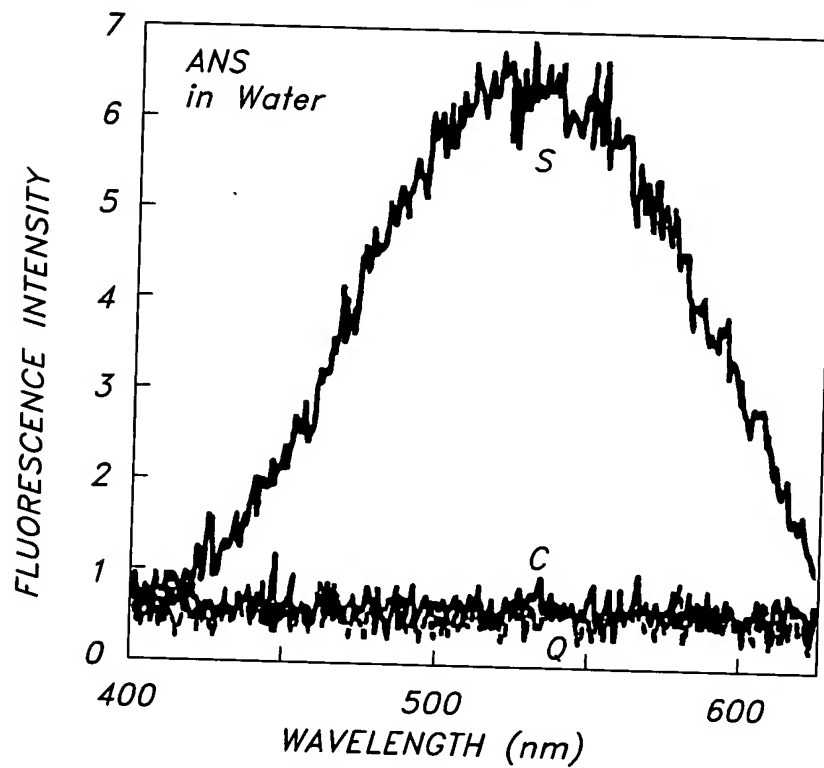
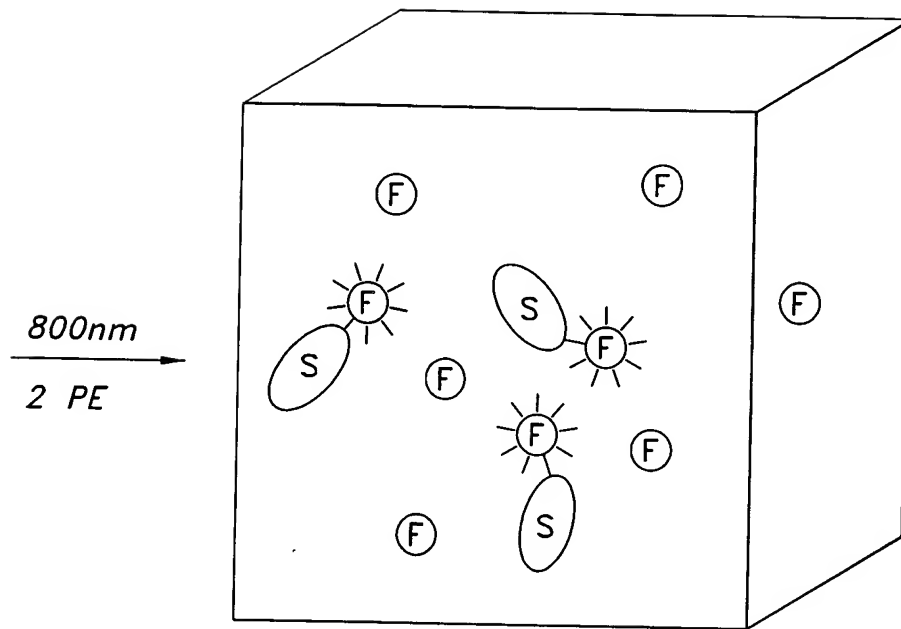
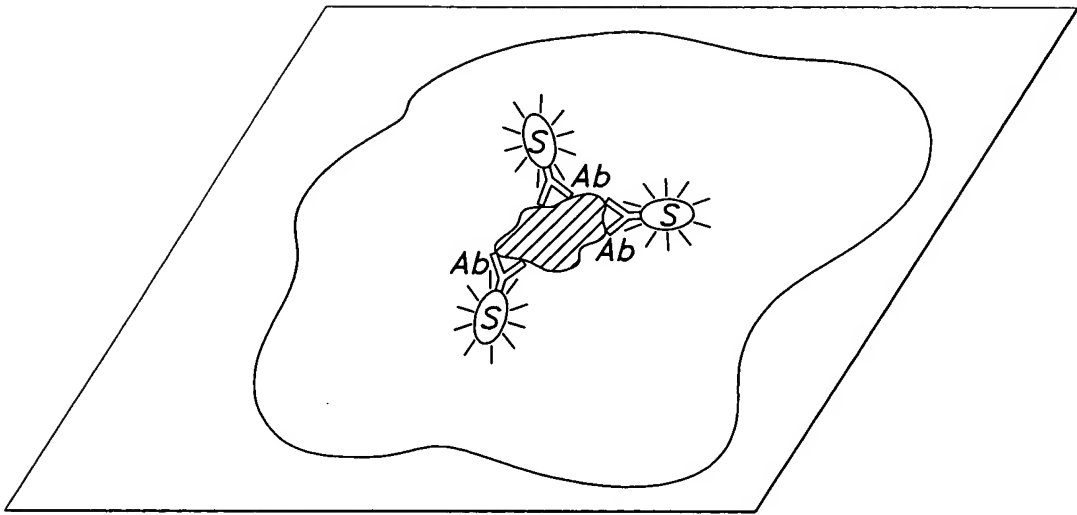
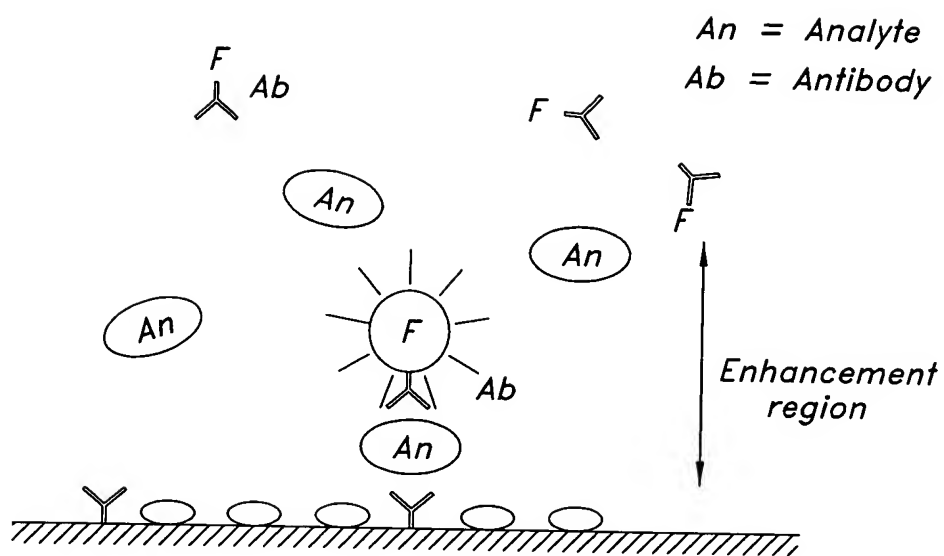


FIG 32B

**FIG 33**

**FIG 34**

**FIG 35**

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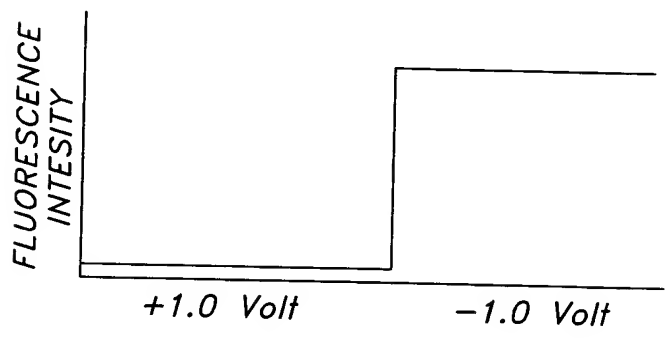
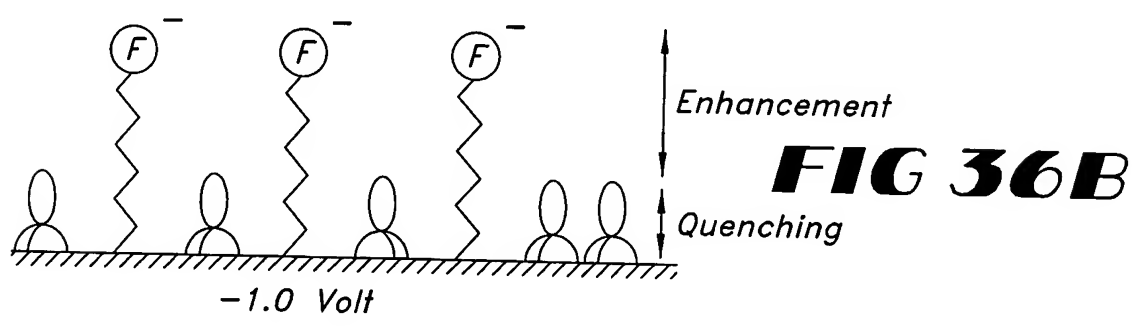
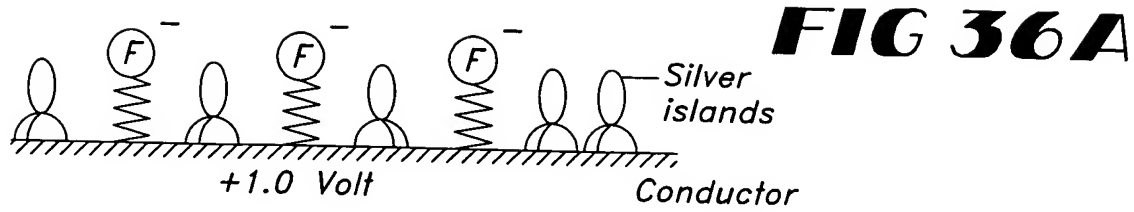
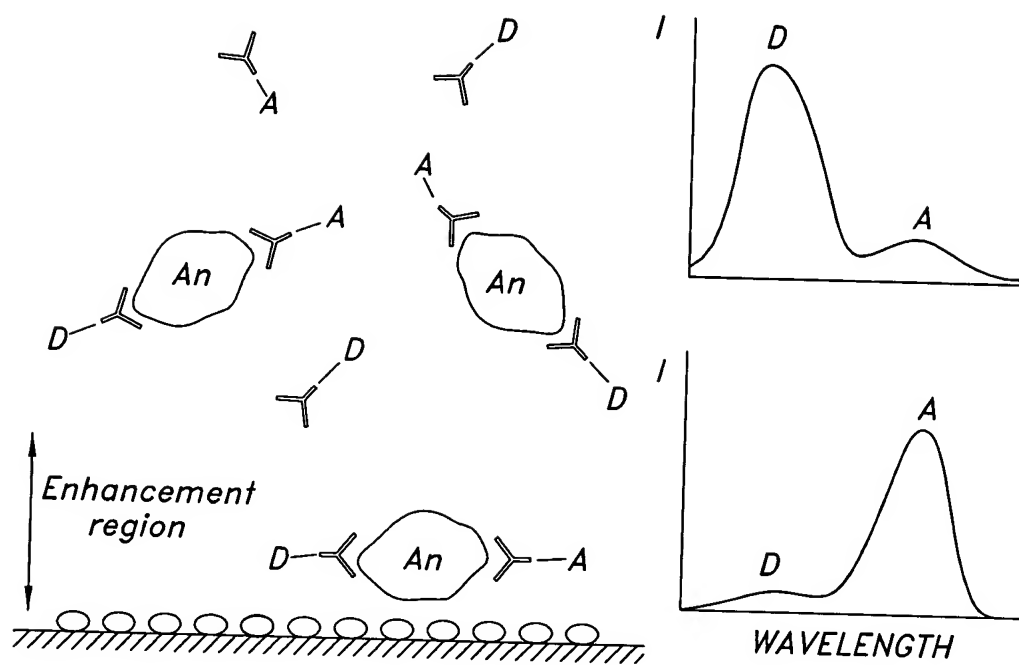
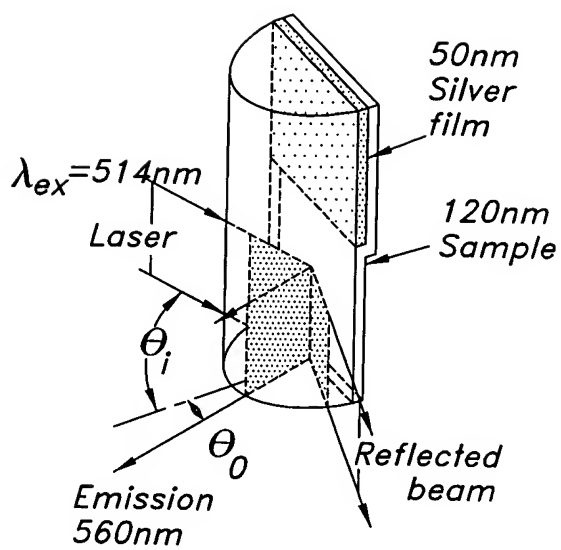
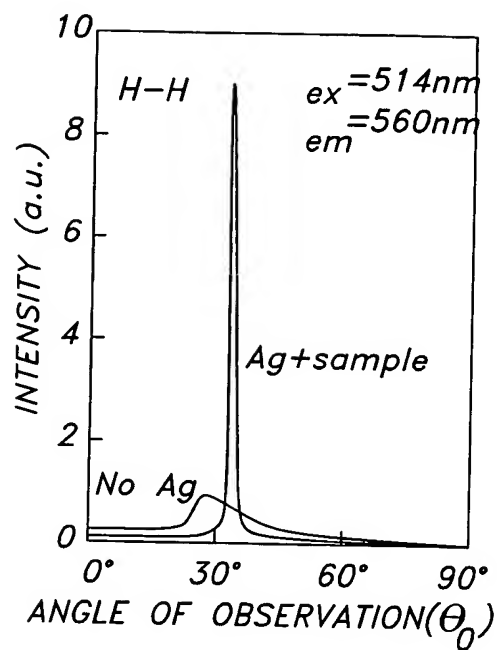


FIG 36A

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FIG 37B**FIG 37A****FIG 37C**

**FIG 38A****FIG 38B**